Features

- Low gate charge
- 100% avalanche tested
- Improved dv/dt capability
- RoHS compliant
- Halogen free package
- JEDEC Qualification
- Improved ESD performance

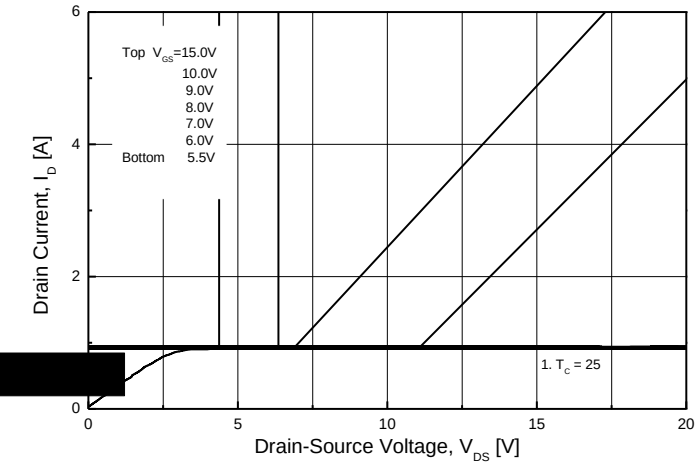
Absolute Maximum Ratings

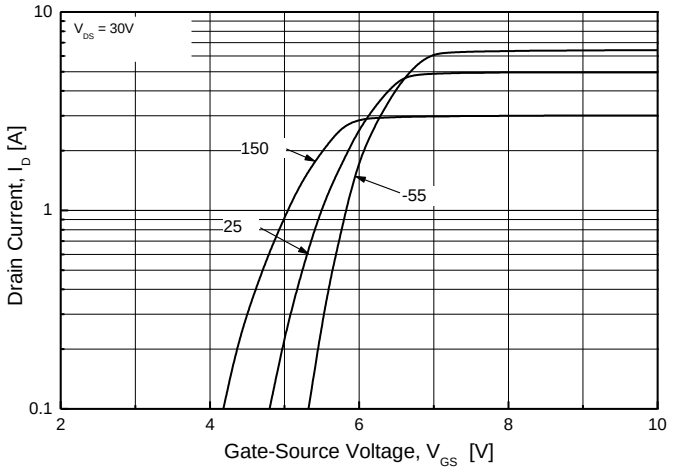
Parameter		Symbol	TMP2N60AZ(G)	TMPF2N60AZ(G)	Unit
Drain-Source Voltage		V_{DSS}	600		V
Gate-Source Voltage		V_{GS}	30		V
Continuous Drain Current	T_C = 25	I_D	2.0	2.0 *	A
	T_C = 100		1.43	1.43 *	A
Pulsed Drain Current (Note 1)		I_{DM}	8	8 *	A
Single Pulse Avalanche Energy (Note 2)		E_{AS}	66		mJ
Repetitive Avalanche Current (Note 1)		I_{AR}	2.0		A
Repetitive Avalanche Energy (Note 1)		E_{AR}	5.21		mJ
Power Dissipation	T_C = 25	P_D	52.1	17.3	W
	Derate above 25		0.416	0.138	W/
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.5		V/ns
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150		
Maximum lead temperature for soldering purposes,					

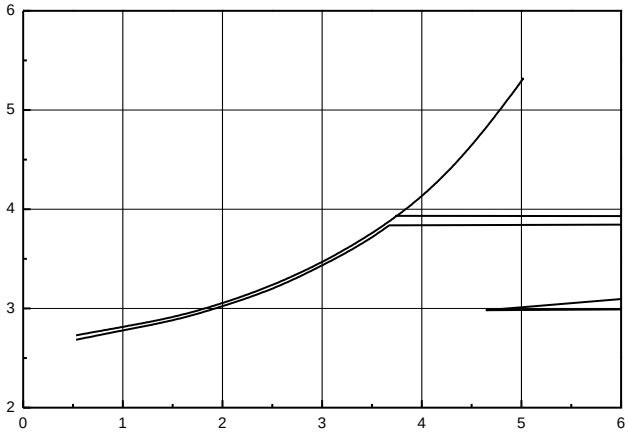
Electrical Characteristics : T_C=25 , unless otherwise noted

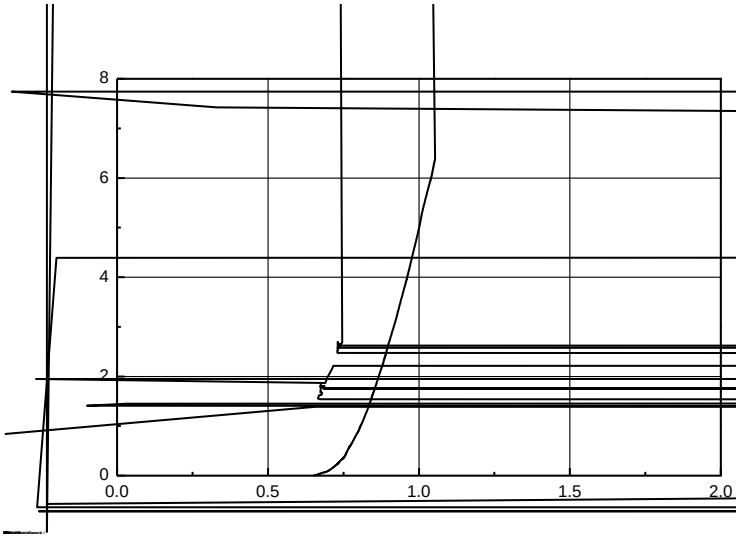
Parameter	Symbol	Test condition	Min	Typ	Max	Units
OFF						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS} = 0 V, I_D = 250 µA	600	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 600 V, V_{GS} = 0 V	--	--	1	µA
		V_{DS} = 480 V, T_C = 125°C	--	--	10	µA
Forward Gate-Source Leakage Current	I_{GSSF}	V_{GS} = 30 V, V_{DS} = 0 V	--	--	100	µA
Reverse Gate-Source Leakage Current	I_{GSSR}	V_{GS} = -30 V, V_{DS} = 0 V	--	--	-100	µA
ON						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	3	--	5	V
Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 1.0 A	--	3.2	4.0	Ω
Forward Transconductance (Note 4)	g_{FS}	V_{DS} = 30 V, I_D = 1.0 A	--	3	--	S
DYNAMIC						
Input Capacitance	C_{iss}	V_{DS} = 25 V, V_{GS} = 0 V, f = 1.0 MHz	--	360	--	pF
Output Capacitance	C_{oss}		--	41	--	pF
Reverse Transfer Capacitance	C_{rss}		--	7	--	pF
SWITCHING						
Turn-On Delay Time (Note 4,5)	$t_{d(on)}$	V_{DD} = 300 V, I_D = 2.0 A, R_G = 25 , V_{GS} = 10 V	--	20	--	ns
Turn-On Rise Time (Note 4,5)	t_r		--	23	--	ns
Turn-Off Delay Time (Note 4,5)	$t_{d(off)}$		--	42	--	ns
Turn-Off Fall Time (Note 4,5)	t_f		--	20	--	ns
Total Gate Charge (Note 4,5)	Q_g	V_{DS} = 480 V, I_D = 2.0 A, V_{GS} = 10 V	--	9	--	nC
Gate-Source Charge (Note 4,5)	Q_{gs}		--	2	--	nC
Gate-Drain Charge (Note 4,5)	Q_{gd}		--	4	--	nC
SOURCE DRAIN DIODE						
Maximum Continuous Drain-Source Diode Forward Current	I_S	----	--	--	2.0	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}	----	--	--	8	A
Drain-Source Diode Forward Voltage	V_{SD}	V_{GS} = 0 V, I_S = 2.0 A	--	--	1.5	V
Reverse Recovery Time (Note 4)	t_{rr}	V_{GS} = 0 V, I_S = 2.0 A, dI_F / dt = 100 A/µs	--	240	--	ns
Reverse Recovery Charge (Note 4)	Q_{rr}		--	0.8	--	µC

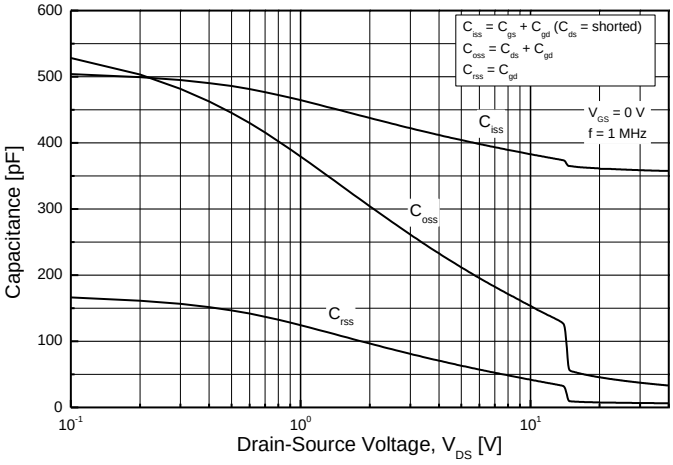
Note :
1. Repeated rating : Pulse width limited by safe operating area
2. L=30.5mH, I_{AS} = 2.0A, V_{DD} = 50V, R_G = 25 , Starting T_J= 25
3 I_{SD} 2.0A, di/dt µs , V_{DD} $_{DS}$, Starting T_J= 25
µ
5. Essentially Independent of Operating Temperature Typical Characteristics

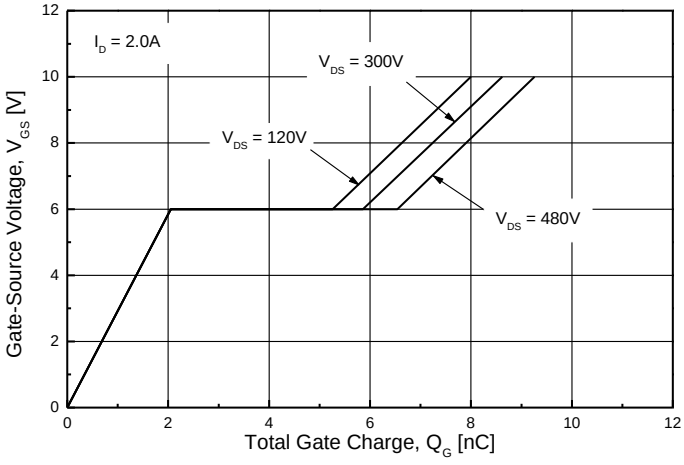

Top V_{GS}=15.0V
10.0V
9.0V
8.0V
7.0V
6.0V
Bottom 5.5V
1. T_C = 25
Drain Current, I_D [A]
Drain-Source Voltage, V_{DS} [V]

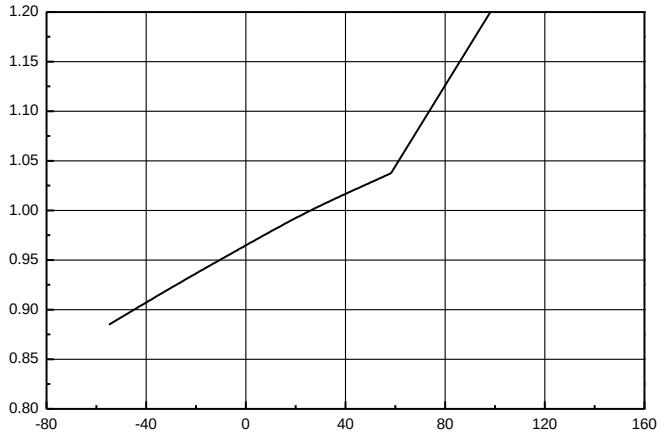

V_{DS} = 30V
150
-55
25
Drain Current, I_D [A]
Gate-Source Voltage, V_{GS} [V]

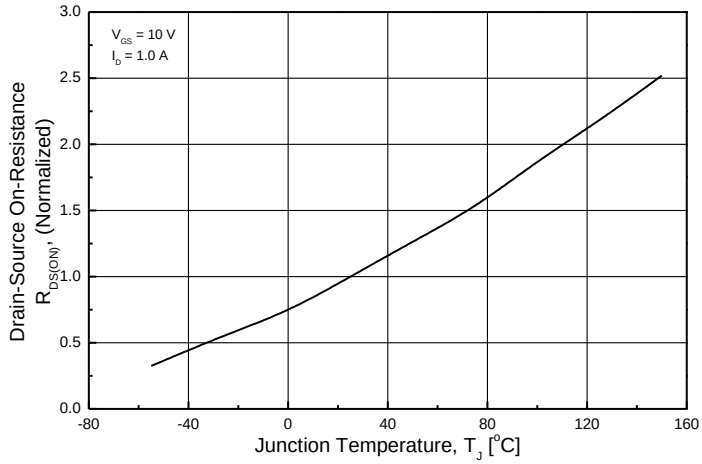


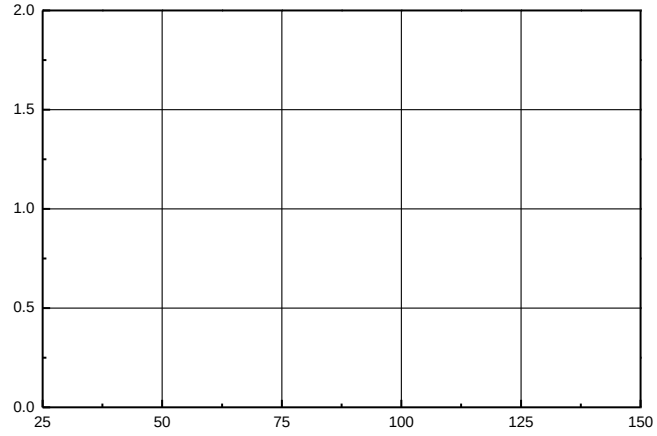


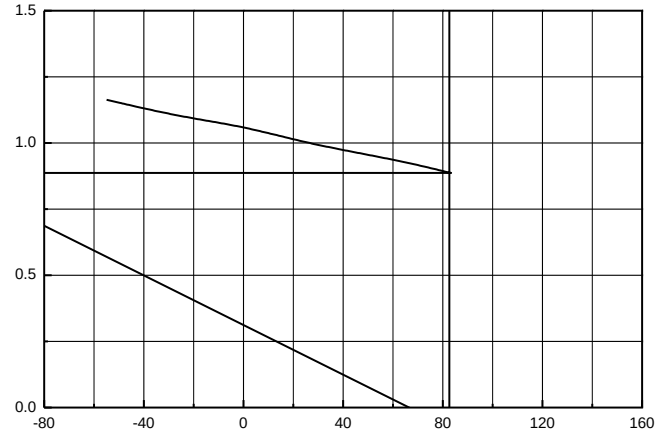

$C_{iss} = C_{gs} + C_{gd}$ (C_{ds} = shorted)
$C_{oss} = C_{ds} + C_{gd}$
$C_{rss} = C_{gd}$
V_{GS} = 0 V
f = 1 MHz
C_{iss}
C_{oss}
C_{rss}
Capacitance [pF]
Drain-Source Voltage, V_{DS} [V]


I_D = 2.0A
V_{DS} = 300V
V_{DS} = 120V
V_{DS} = 480V
Gate-Source Voltage, V_{GS} [V]
Total Gate Charge, Q_G [nC]

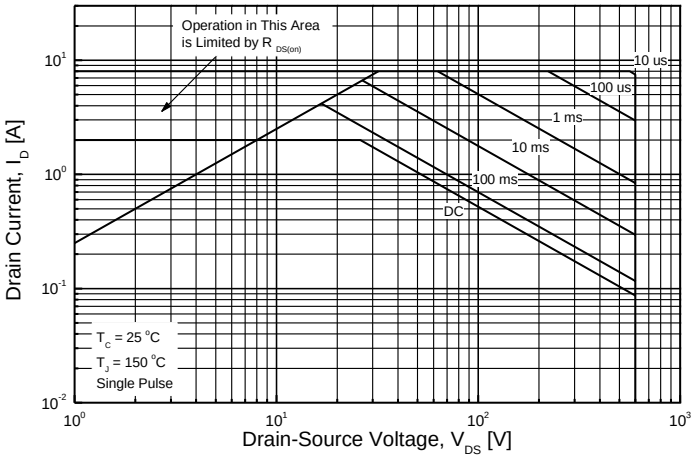








TMP2N60AZ(G)



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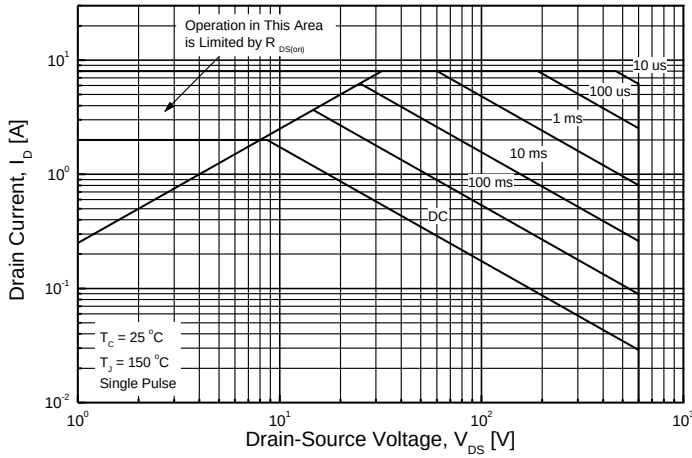

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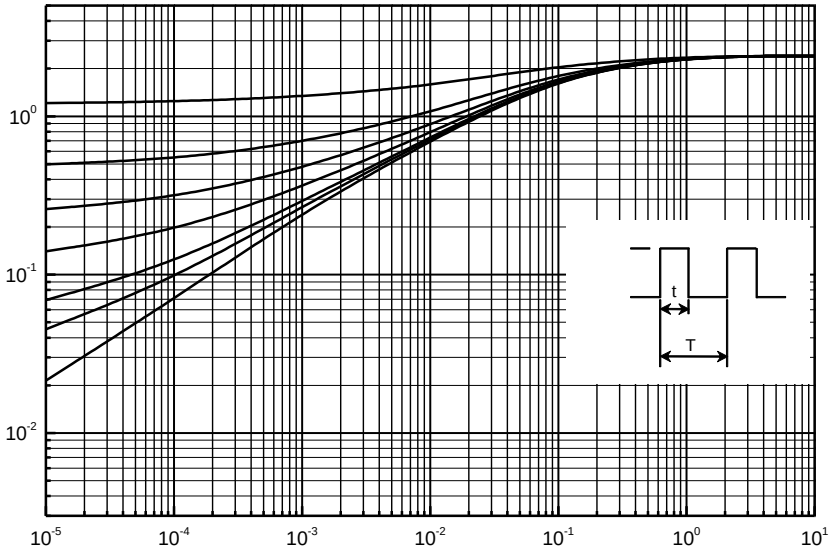

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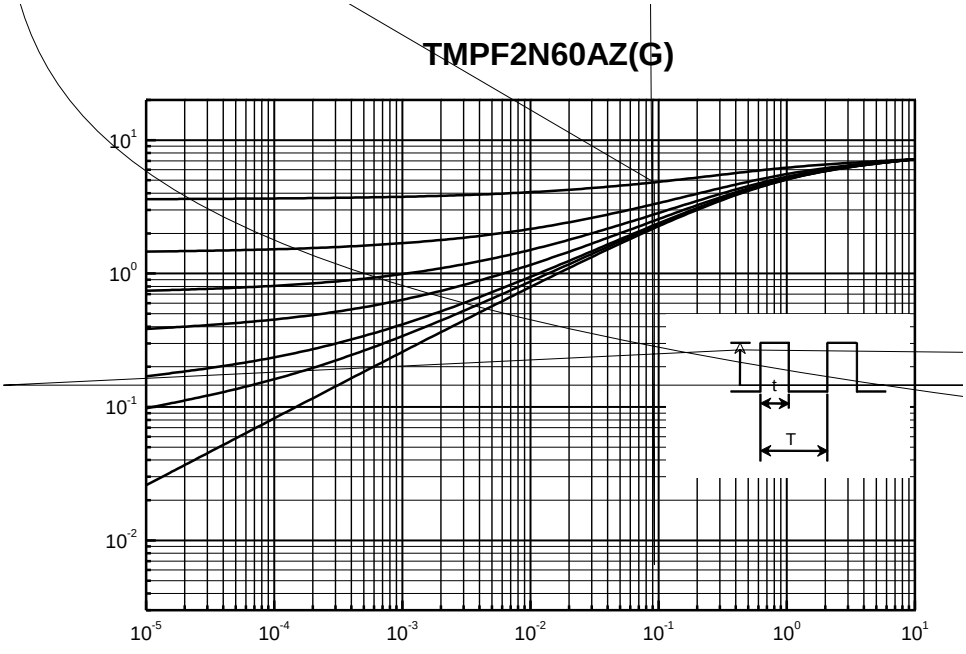
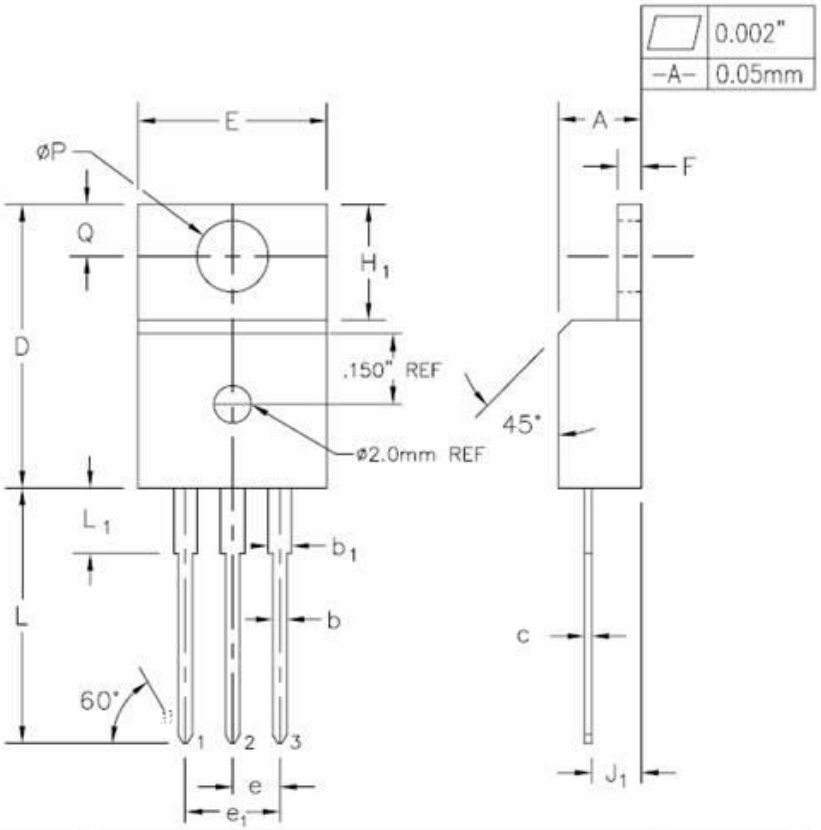


TO-220AB-3L MECHANICAL DATA



SYMBOL	INCHES		MILLIMETERS		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	0.170	0.180	4.32	4.57	
b	0.028	0.036	0.71	0.91	
b1	0.045	0.055	1.15	1.39	
c	0.014	0.021	0.36	0.53	
D	0.590	0.610	14.99	15.49	
