General Description

SFGMOS® low $R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. The low V_{th} series is specially designed to use in synchronous rectification power systems with low driving voltage.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent reliability and uniformity
- Fast switching and soft recovery

Applications

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC convertor
- Switched mode power supply

Key Performance Parameters

Parameter	Value	Unit
$V_{DS,\ min}$ @ $T_{j(max)}$	100	V
$I_{D,\ pulse}$	15	A
$R_{DS(ON)\ max}$ @ V_{GS}=10V	75	
[redacted]	6.5	nC

Absolute Maximum Ratings at T_j=25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	V_{DS}	100	V
Gate source voltage	V_{GS}	±20	V
Continuous drain current[1], T_C=25 °C	I_D	3	A
Pulsed drain current[2], T_C=25 °C	$I_{D, pulse}$	15	A
Continuous diode forward current[1], T_C=25 °C	I_S	3	A
Diode pulsed current[2], T_C=25 °C	$I_{S, Pulse}$	15	A
Power dissipation[3], T_C=25 °C	P_D	2	W
Single pulsed avalanche energy[5]	E_{AS}	5.5	mJ
Operation and storage temperature	T_{stg} T_j	-	

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		310		pF	V_{GS}=0 V, V_{DS}=25 V, kHz
Output capacitance	C_{oss}		171		pF	
Reverse transfer capacitance	C_{rss}		16.7		pF	
Turn-on delay time	$t_{d(on)}$		14		ns	V_{GS}=10 V, V_{DS}=50 V, R_G I_D=5 A
Rise time	t_r		3.2		ns	
Turn-off delay time	$t_{d(off)}$		36		ns	
Fall time	t_f					

Electrical Characteristics Diagrams

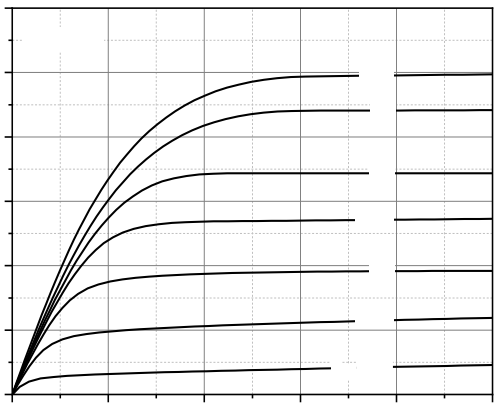


Figure 1. Typ. output characteristics

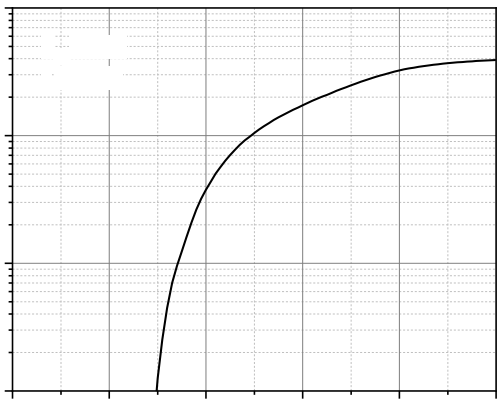


Figure 2. Typ. transfer characteristics

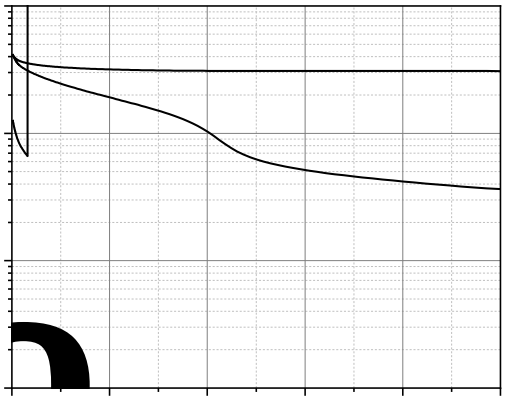


Figure 3. Typ. capacitances

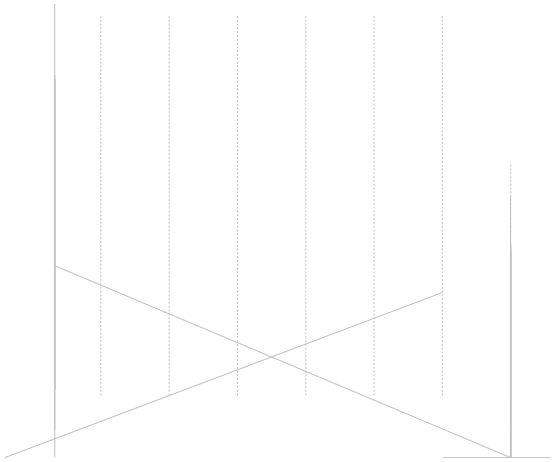


Figure 4. Typ. gate charge

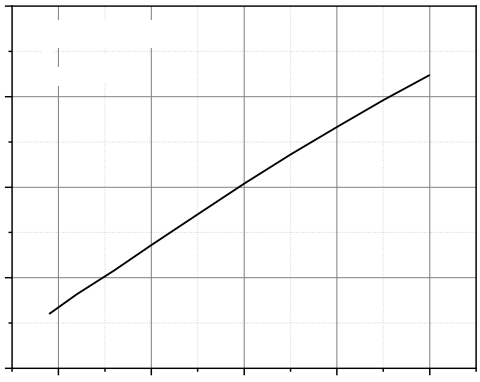


Figure 5. Drain-source breakdown voltage

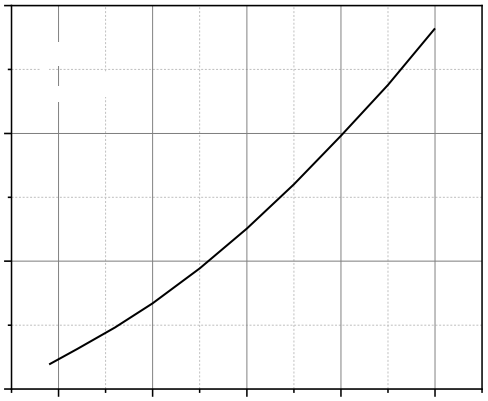


Figure 6. Drain-source on-state resistance

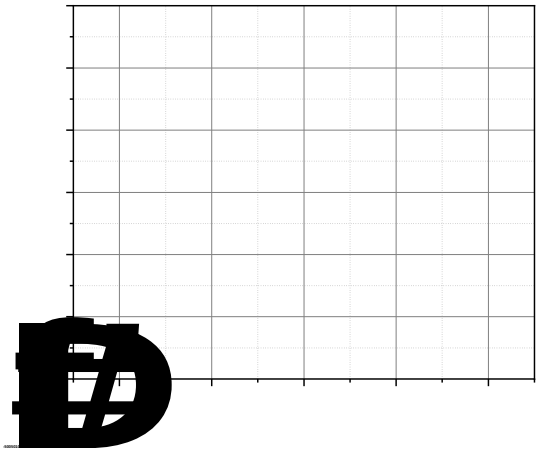


Figure 7. Threshold voltage

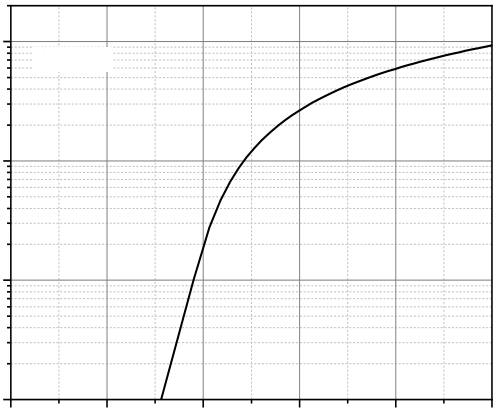


Figure 8. Forward characteristic of body diode

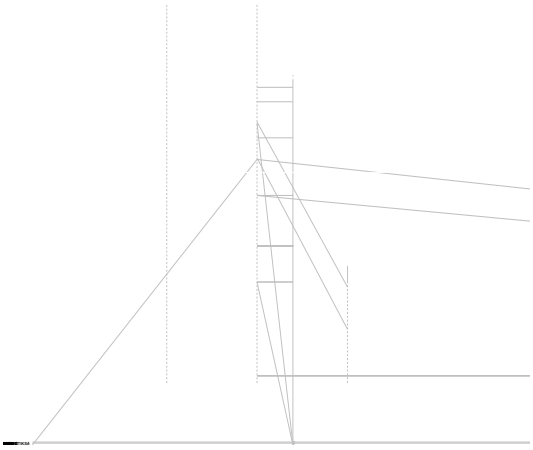


Figure 9. Drain-source on-state resistance

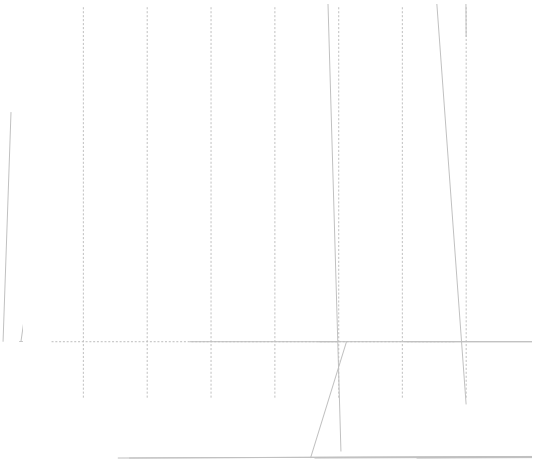


Figure 10. Drain current

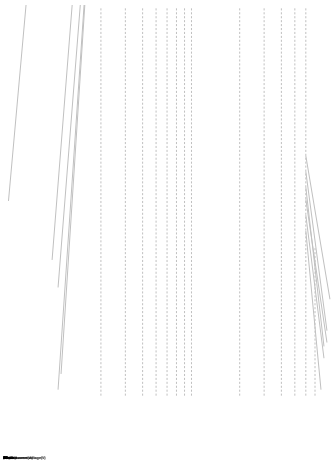


Figure 11. Safe operation area T_C=25 °C

Test circuits and waveforms

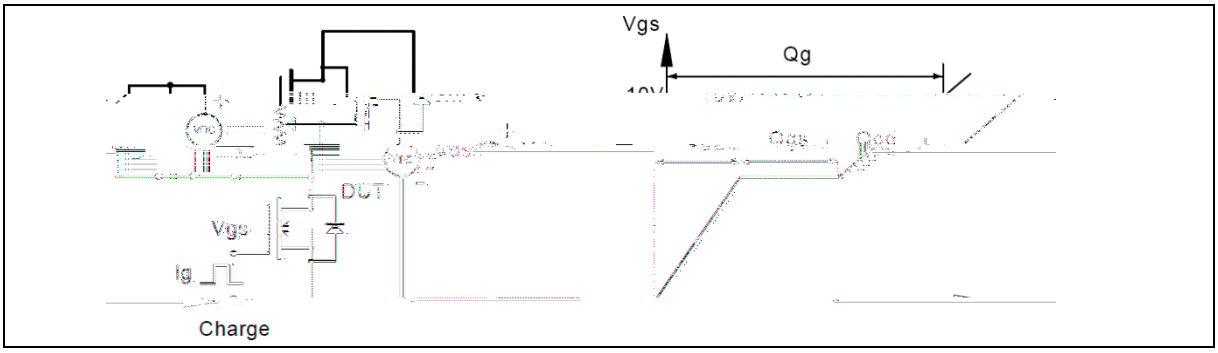


Figure 1. Gate charge test circuit & waveform

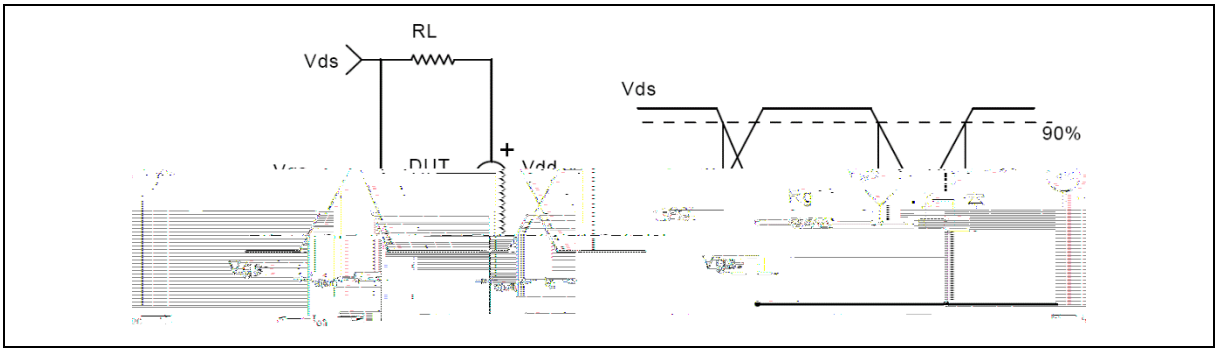


Figure 2. Switching time test circuit & waveforms

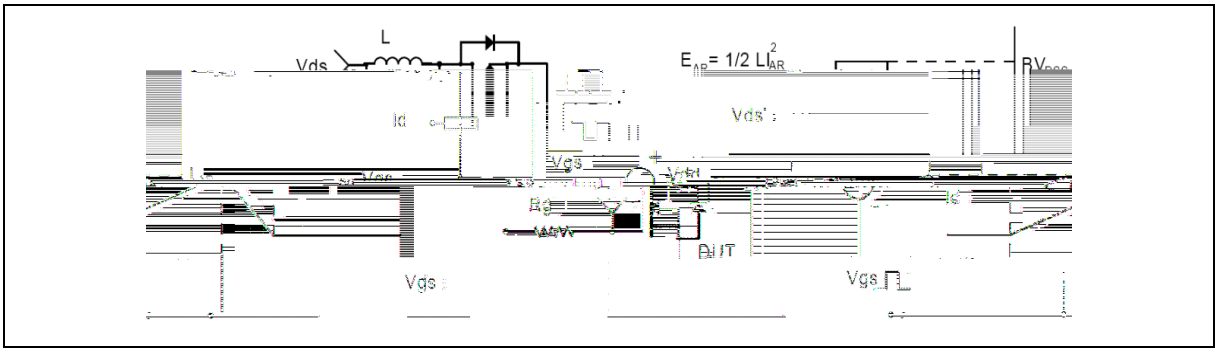


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

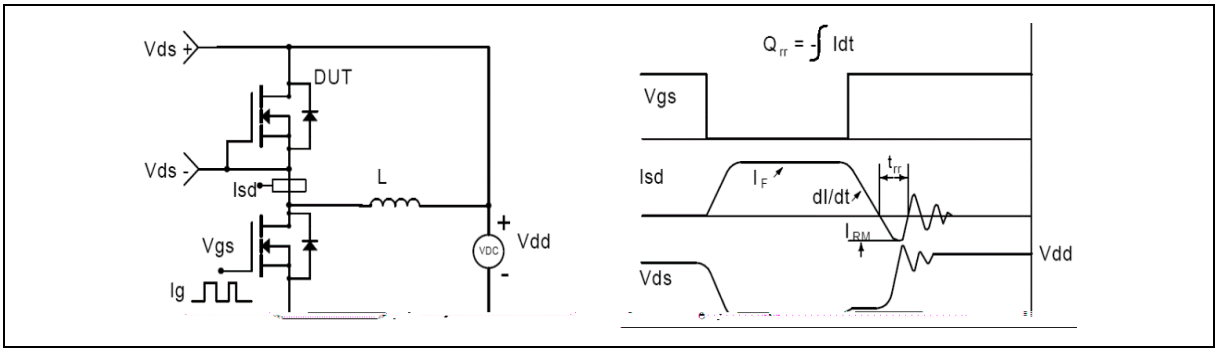
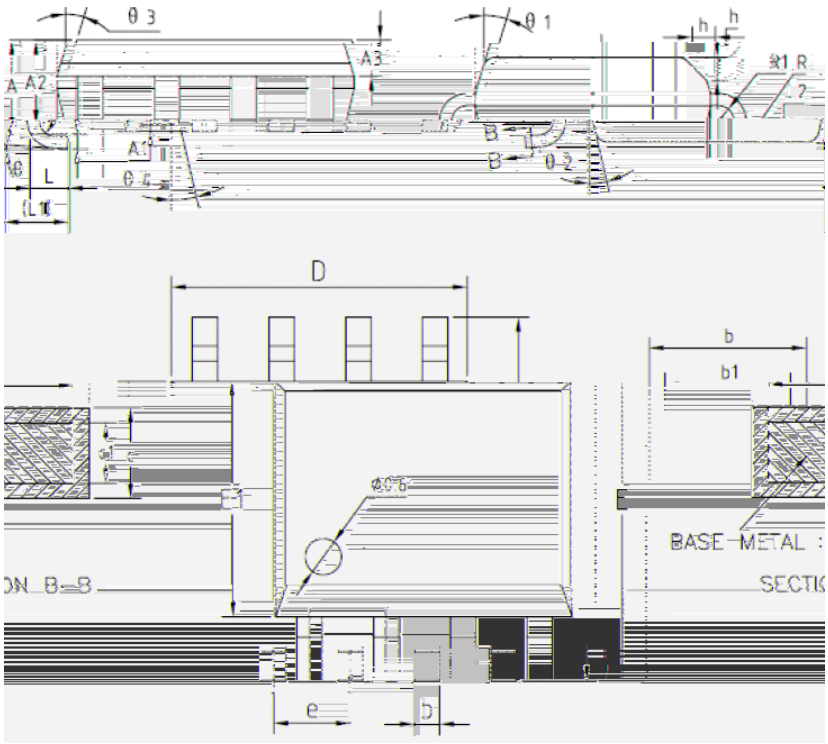


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	1.35	1.55	1.75
A1	0.10	0.15	0.25
A2	1.25	1.40	1.65
A3	0.50	0.60	0.70
b	0.38	-	0.51
L1	1.04REF		
L2	0.25BSC		
b1	0.37	0.42	0.47
c	0.18	-	0.25
c1	0.17	0.20	0.23
D	4.80	4.90	5
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.17	1.27	1.37
L	0.45	0.60	0.80
R	0.07	-	-
R1	0.07	-	-
h	0.30	0.40	0.50
	0	-	

Version 1: SOP8-K package outline dimension

Ordering Information

Package Type	Units/ Reel	Reels / Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
SOP8-K	2500	2	5000	6	30000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free