

low

$R_{DS(ON)}$ , low gate charge, fast switching and excellent avalanche characteristics. The low  $V_{th}$  series is specially designed to use in synchronous rectification power systems with low driving voltage.

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- Switched mode power supply



Parameter	Value	Unit
$V_{DS, min} @ T_j(max)$	60	V
$I_D, pulse$	480	A
$R_{DS(ON) max} @ V_{GS}=10V$	3.5	
$Q_g$	66.1	nC

Product Name	Package	Marking
SFS06R03DF	TO252	SFS06R03D



**Absolute Maximum Ratings** at  $T_j=25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	$V_{DS}$	60	V
Gate source voltage	$V_{GS}$	$\pm 20$	V
Continuous drain current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_D$	160	A
Pulsed drain current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{D, \text{pulse}}$	480	A
Continuous diode forward current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_S$	160	A
Diode pulsed current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{S, \text{Pulse}}$	480	A
Power dissipation <sup>3)</sup> , $T_C=25^\circ\text{C}$	$P_D$	168	W
Single pulsed avalanche energy <sup>5)</sup>	$E_{AS}$	200	mJ
Operation and storage temperature	$T_{stg} \quad T_j$	-55 to 175	$^\circ\text{C}$

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	R	0.89	$^\circ\text{C}/\text{W}$
Thermal resistance, junction-ambient <sup>4)</sup>	R	62	$^\circ\text{C}/\text{W}$

**Electrical Characteristics** at  $T_j=25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	$BV_{DSS}$	60			V	$V_{GS}=0 \text{ V}, I_D=250 \text{ A}$
Gate threshold voltage	$V_{GS(\text{th})}$	1.3		2.5	V	$V_{DS}=V_{GS}, I_D=250 \text{ A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		3.0	3.5		$V_{GS}=10 \text{ V}, I_D=20 \text{ A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		3.5	4.5		$V_{GS}=4.5 \text{ V}, I_D=10 \text{ A}$
Gate-source leakage current	$I_{GSS}$			100	nA	$V_{GS}=20 \text{ V}$
				-100		$V_{GS}=-20 \text{ V}$
Drain-source leakage current	$I_{DSS}$			1	A	$V_{DS}=60 \text{ V}, V_{GS}=0 \text{ V}$

### Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C <sub>iss</sub>		5377		pF	V <sub>GS</sub> =0 V, V <sub>DS</sub> =25 V, 211 kHz
Output capacitance	C <sub>oss</sub>		1666		pF	
Reverse transfer capacitance	C <sub>rss</sub>		77.7		pF	
Turn-on delay time	t <sub>d(on)</sub>		22.5		ns	V <sub>GS</sub> =10 V, V <sub>DS</sub> =30 V, R <sub>G</sub> - I <sub>D</sub> =25 A
Rise time	t <sub>r</sub>		6.7		ns	
Turn-off delay time	t <sub>d(off)</sub>		80.3		ns	
Fall time	t <sub>f</sub>		26.8		ns	

### Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q <sub>g</sub>		66.1		nC	V <sub>GS</sub> =10 V, V <sub>DS</sub> =30 V, I <sub>D</sub> =25 A
Gate-source charge	Q <sub>gs</sub>		10.7		nC	
Gate-drain charge	Q <sub>gd</sub>		10.9		nC	
Gate plateau voltage	V <sub>plateau</sub>		2.9		V	

### Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V <sub>SD</sub>			1.3	V	I <sub>S</sub> =20 A, V <sub>GS</sub> =0 V
Reverse recovery time	t <sub>rr</sub>		68.3		ns	V <sub>R</sub> =30 V, I <sub>S</sub> =25 A, 0 211 0
Reverse recovery charge	Q <sub>rr</sub>		73.0		nC	
Peak reverse recovery current	I <sub>rrm</sub>		1.9		A	

### Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of R<sub>G</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25 °C.
- 5) V<sub>DD</sub>=30 V, V<sub>GS</sub>=10 V, L=0.3 mH, starting T<sub>j</sub>=25 °C.

### Electrical Characteristics Diagrams

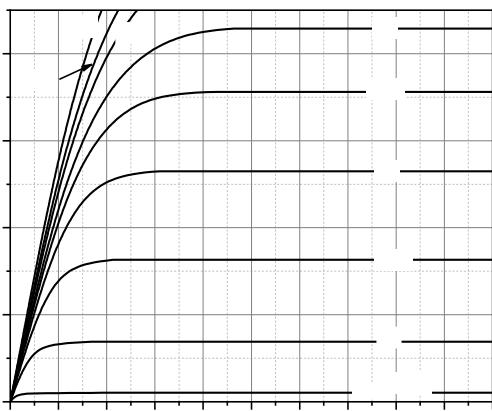


Figure 1. Typ. output characteristics

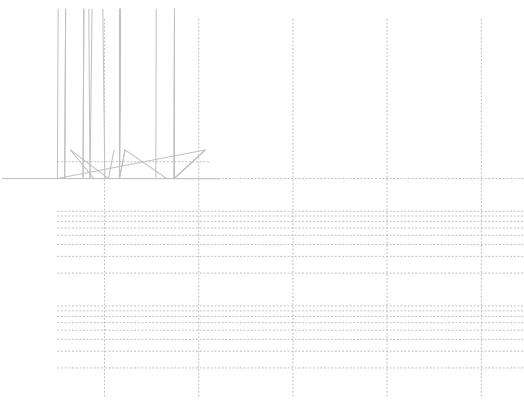


Figure 2. Typ. transfer characteristics

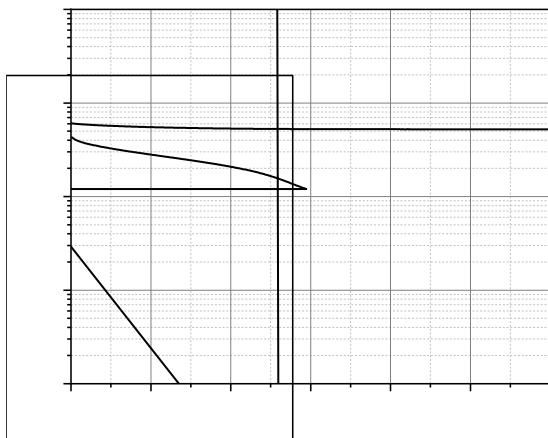


Figure 3. Typ. capacitances

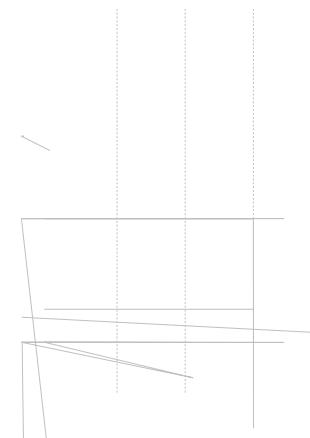


Figure 4. Typ. gate charge

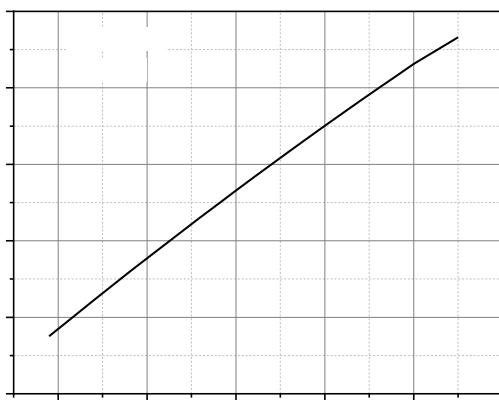


Figure 5. Drain-source breakdown voltage

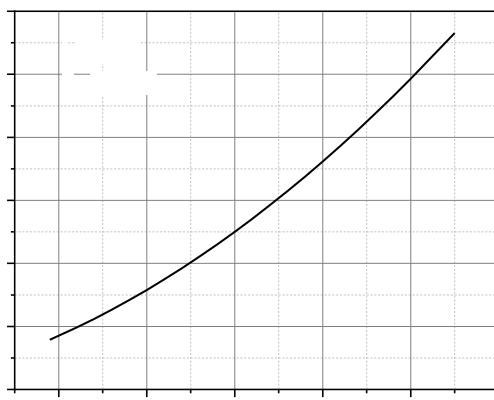
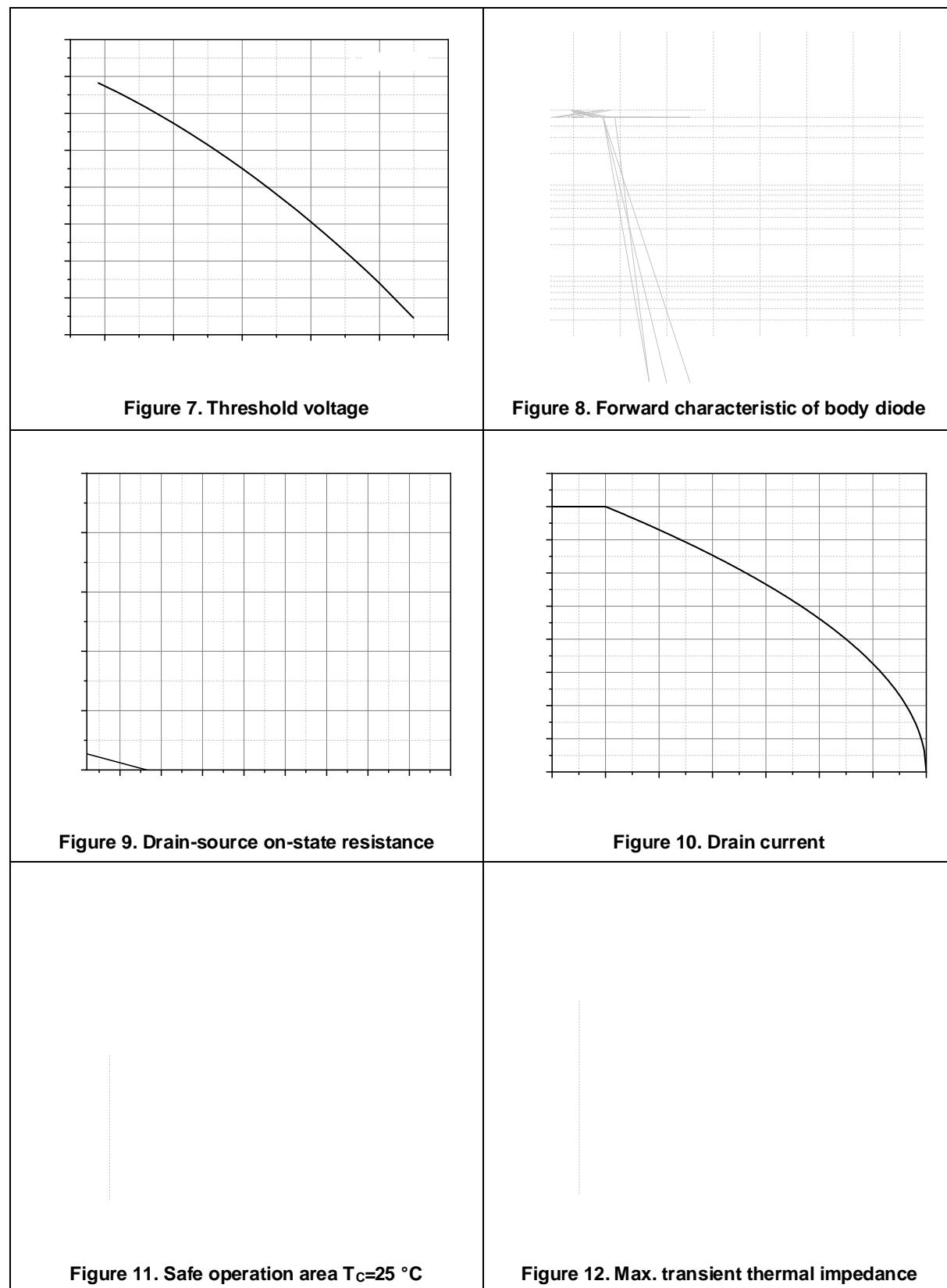


Figure 6. Drain-source on-state resistance



**SFS06R03DF**

Enhancement Mode N-Channel Power MOSFET



## Package Information

**SFS06R03DF**

Enhancement Mode N-

## Ordering Information

Package Type	Units/Reel	Reels / Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO252-J	2500	2	5000	5	25000
TO252-P	2500	2	5000	5	25000

## Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFS06R03DF	TO252	yes	yes	yes

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