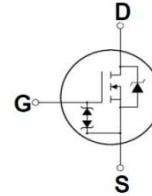
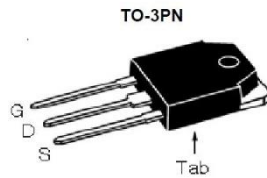


## Features

- Low gate charge
- 100% avalanche tested
- Improved dv/dt capability
- RoHS compliant
- JEDEC Qualification

$BV_{DSS}$	$I_D$	$R_{DS(on)MAX}$
900V	11A	<0.95 $\Omega$



Device	Package	Marking	Remark
TMAN11N90Z	TO-3PN	TMAN11N90Z	RoHS

## Absolute Maximum Ratings

Parameter	Symbol	TMAN11N90Z	Unit
Drain-Source Voltage	$V_{DS}$	900	V
Gate-Source Voltage	$V_{GS}$	30	V
Continuous Drain Current	$I_D$	$T_C = 25$	11
		$T_C = 100$	8.5
Pulsed Drain Current (Note 1)	$I_{DM}$	44	A
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	360	mJ
Repetitive Avalanche Current (Note 1)	$I_{AR}$	11	A
Repetitive Avalanche Energy (Note 1)	$E_{AR}$	41.6	mJ
Power Dissipation	$P_D$	$T_C = 25$	416
		Derate above 25	3.3
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5	V/ns
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	
Maximum lead temperature for soldering purposes,	$T_L$	300	

\* Limited only by maximum junction temperature

## Thermal Characteristics

Parameter	Symbol	TMAN11N90Z	Unit
Maximum Thermal resistance, Junction-to-Case	$R_{\theta JC}$	0.3	/W
Maximum Thermal resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	/W

**Electrical Characteristics :  $T_C=25$  , unless otherwise noted**

Parameter	Symbol	Test condition	Min	Typ	Max	Units
<b>OFF</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	900	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 900\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	$\mu\text{A}$
		$V_{DS} = 720\text{ V}, T_C = 125^\circ\text{C}$	--	--	10	$\mu\text{A}$
Forward Gate-Source Leakage Current	$I_{GSSF}$	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	$\mu\text{A}$
Reverse Gate-Source Leakage Current	$I_{GSSR}$	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	$\mu\text{A}$

<b>ON</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	2	--	4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 5.5\text{ A}$	--	0.75	0.95	$\Omega$
Forward Transconductance <sup>(Note 4)</sup>	$g_{FS}$	$V_{DS} = 30\text{ V}, I_D = 5.5\text{ A}$	--	8	--	S

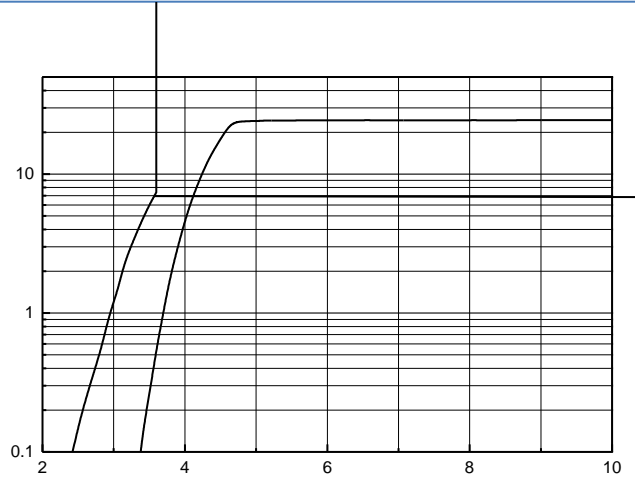
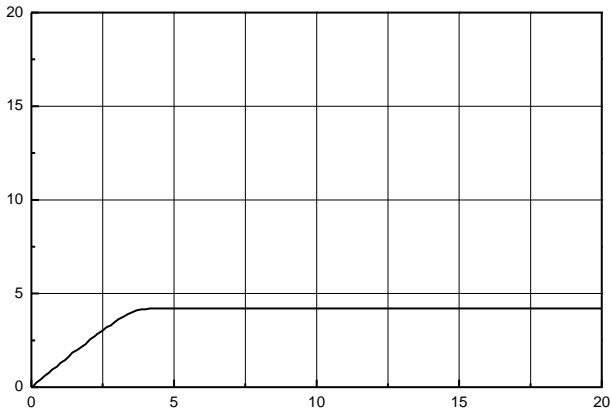
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	3737	--	pF
Output Capacitance	$C_{oss}$		--	267	--	pF
Reverse Transfer Capacitance	$C_{rss}$		--	32	--	pF

<b>SWITCHING</b>						
Turn-On Delay Time <sup>(Note 4,5)</sup>	$t_{d(on)}$	$V_{DD} = 450\text{ V}, I_D = 11\text{ A},$ $R_G = 25$	--	59	--	ns
Turn-On Rise Time <sup>(Note 4,5)</sup>	$t_r$		--	62	--	ns
Turn-Off Delay Time <sup>(Note 4,5)</sup>	$t_{d(off)}$		--	408	--	ns
Turn-Off Fall Time <sup>(Note 4,5)</sup>	$t_f$		--	83	--	ns
Total Gate Charge <sup>(Note 4,5)</sup>	$Q_g$	$V_{DS} = 720\text{ V}, I_D = 11\text{ A},$ $V_{GS} = 10\text{ V}$	--	95	--	nC
Gate-Source Charge <sup>(Note 4,5)</sup>	$Q_{gs}$		--	13	--	nC
Gate-Drain Charge <sup>(Note 4,5)</sup>	$Q_{gd}$		--	40	--	nC

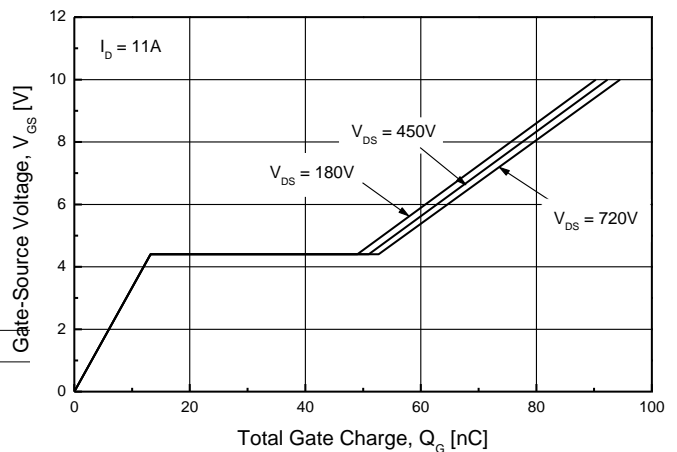
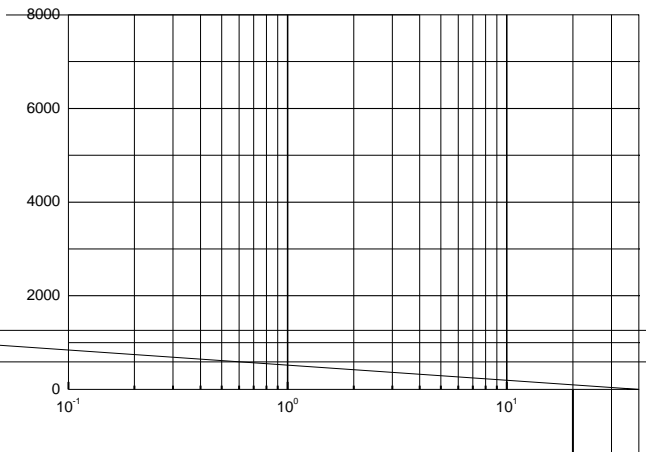
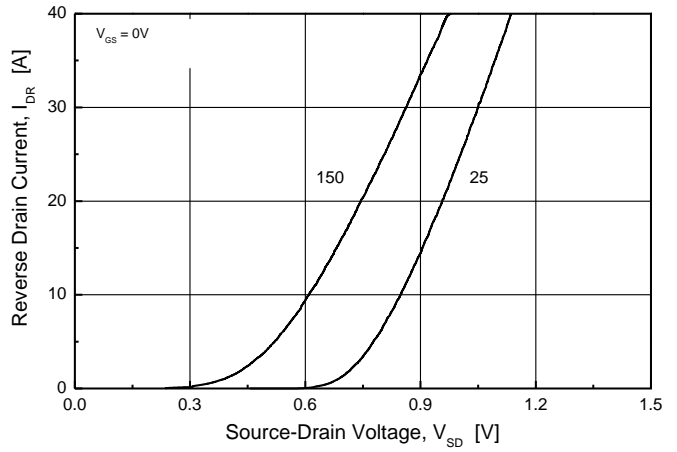
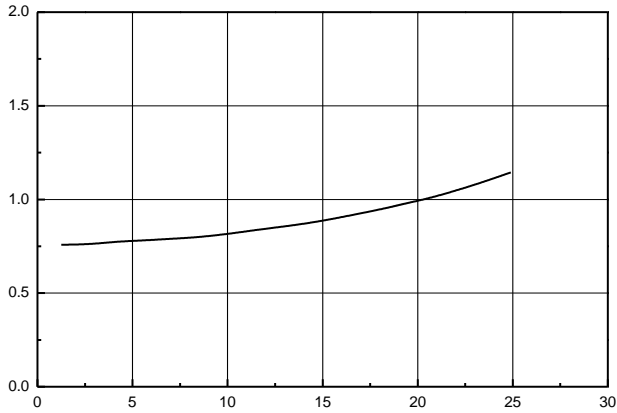
<b>SOURCE DRAIN DIODE</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$	---	--	--	11	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{SM}$	---	--	--	44	A
Drain-Source Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0\text{ V}, I_S = 11\text{ A}$	--	--	1.5	V
Reverse Recovery Time <sup>(Note 4)</sup>	$t_{rr}$	$V_{GS} = 0\text{ V}, I_S = 11\text{ A}$ $di_F / dt = 100\text{ A}/\mu\text{s}$	--	513	--	ns
Reverse Recovery Charge <sup>(Note 4)</sup>	$Q_{rr}$		--	6	--	$\mu\text{C}$

Note :

1. Repeated rating : Pulse width limited by safe operating area
2.  $L=5.6\text{mH}, I_{AS} = 11\text{A}, V_{DD} = 50\text{V}, R_G = 25$  , Starting  $T_j= 25$
3.  $I_{SD} 11\text{A}, di/dt \mu\text{s}, V_{DD} \mu\text{s}, V_{DS},$  Starting  $T_j= 25$
5. Essentially Independent of Operating Temperature Typical Characteristics



25







## TO-3PN MECHANICAL DATA