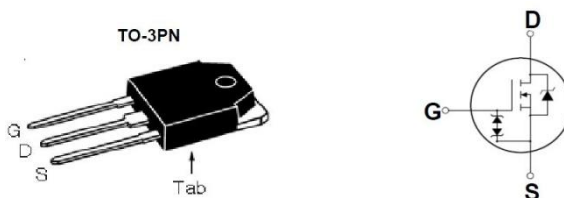


N-channel MOSFET

Features

- Low gate charge
- 100% avalanche tested
- Improved dv/dt capability
- RoHS compliant
- JEDEC Qualification

BV_{DSS}	I_D	$R_{DS(on)MAX}$
800V	12A	<0.65Ω



Device	Package	Marking	Remark
TMAN12N80Z	TO-3PN	TMAN12N80Z	RoHS

Absolute Maximum Ratings

Parameter	Symbol	TMAN12N80Z	Unit
Drain-Source Voltage	V_{DS}	800	V
Gate-Source Voltage	V_{GS}	30	V
Continuous Drain Current	I_D	$T_C = 25$	12
		$T_C = 100$	8.95
Pulsed Drain Current (Note 1)	I_{DM}	48	A
Single Pulse Avalanche Energy (Note 2)	E_{AS}	330	mJ
Repetitive Avalanche Current (Note 1)	I_{AR}	12	A
Repetitive Avalanche Energy (Note 1)	E_{AR}	41.6	mJ
Power Dissipation	P_D	$T_C = 25$	416
		Derate above 25	3.3
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5	V/ns
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	
Maximum lead temperature for soldering purposes,	T_L	300	

* Limited only by maximum junction temperature

Thermal Characteristics

Parameter	Symbol	TMAN12N80Z	Unit
Maximum Thermal resistance, Junction-to-Case	$R_{\theta JC}$	0.3	/W
Maximum Thermal resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	/W

Electrical Characteristics : $T_C=25$, unless otherwise noted

Parameter	Symbol	Test condition	Min	Typ	Max	Units
OFF						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	800	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 800\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	μA
		$V_{DS} = 640\text{ V}, T_C = 125^\circ\text{C}$	--	--	10	μA
Forward Gate-Source Leakage Current	I_{GSSF}	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	μA
Reverse Gate-Source Leakage Current	I_{GSSR}	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	μA

ON

Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	2	--	4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 6\text{ A}$	--	0.53	0.65	Ω
Forward Transconductance ^(Note 4)	g_{FS}	$V_{DS} = 30\text{ V}, I_D = 6\text{ A}$	--	8	--	S

DYNAMIC

Input Capacitance	C_{iss}	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	3723	--	pF
Output Capacitance	C_{oss}		--	295	--	pF
Reverse Transfer Capacitance	C_{rss}		--	32	--	pF

SWITCHING

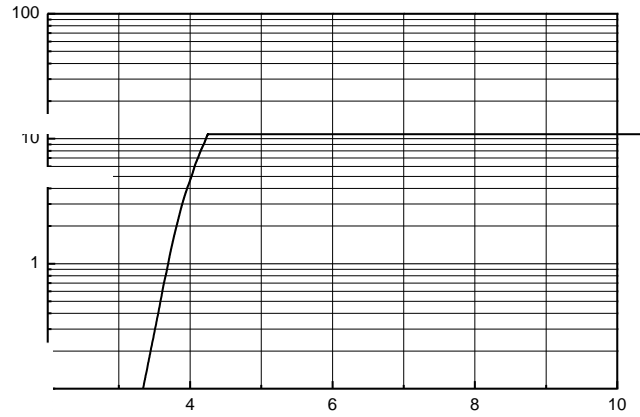
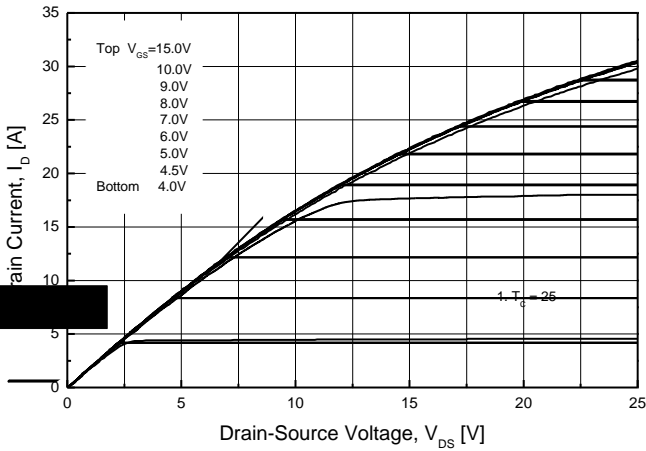
Turn-On Delay Time ^(Note 4,5)	$t_{d(on)}$	$V_{DD} = 400\text{ V}, I_D = 12\text{ A},$ $R_G = 25$	--	59	--	ns
Turn-On Rise Time ^(Note 4,5)	t_r		--	64	--	ns
Turn-Off Delay Time ^(Note 4,5)	$t_{d(off)}$		--	410	--	ns
Turn-Off Fall Time ^(Note 4,5)	t_f		--	94	--	ns
Total Gate Charge ^(Note 4,5)	Q_g	$V_{DS} = 640\text{ V}, I_D = 12\text{ A},$ $V_{GS} = 10\text{ V}$	--	94	--	nC
Gate-Source Charge ^(Note 4,5)	Q_{gs}		--	13	--	nC
Gate-Drain Charge ^(Note 4,5)	Q_{gd}		--	39	--	nC

SOURCE DRAIN DIODE

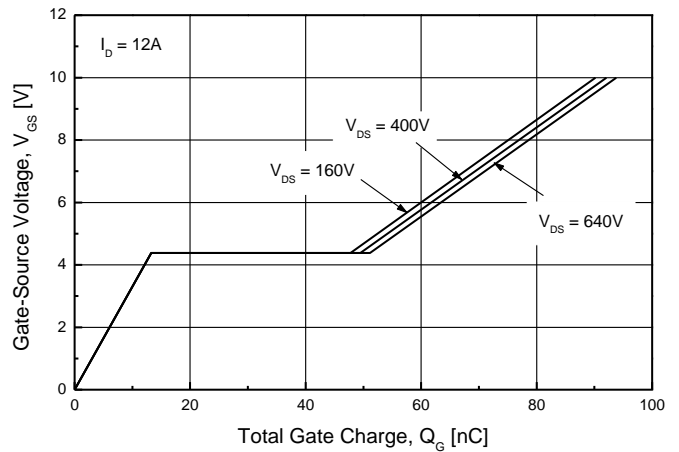
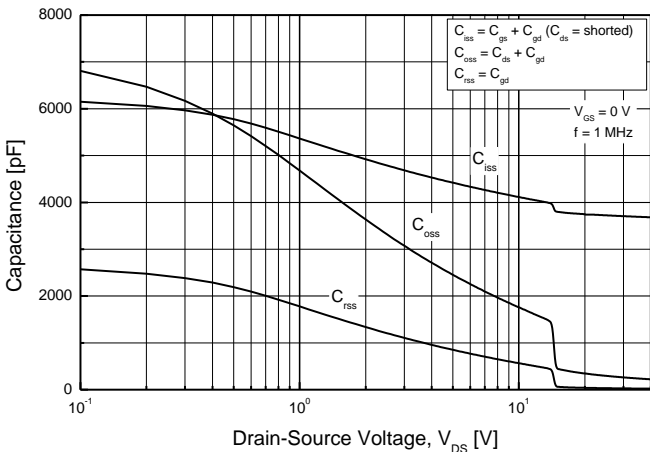
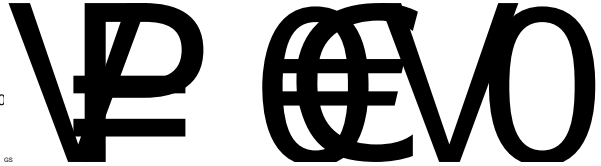
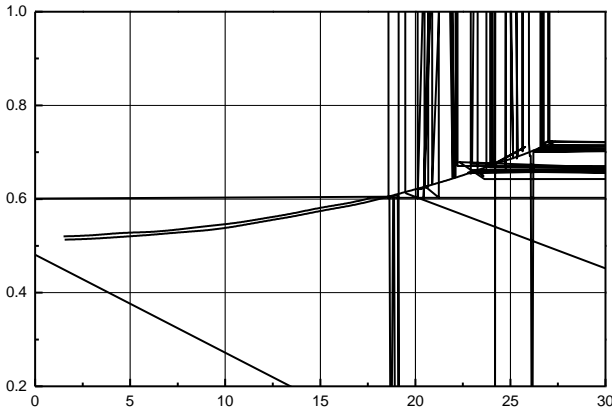
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	--	--	12	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}	---	--	--	48	A
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{ V}, I_S = 12\text{ A}$	--	--	1.5	V
Reverse Recovery Time ^(Note 4)	t_{rr}	$V_{GS} = 0\text{ V}, I_S = 12\text{ A}$	--	369	--	ns
Reverse Recovery Charge ^(Note 4)	Q_{rr}	$di_F / dt = 100\text{ A}/\mu\text{s}$	--	3.4	--	μC

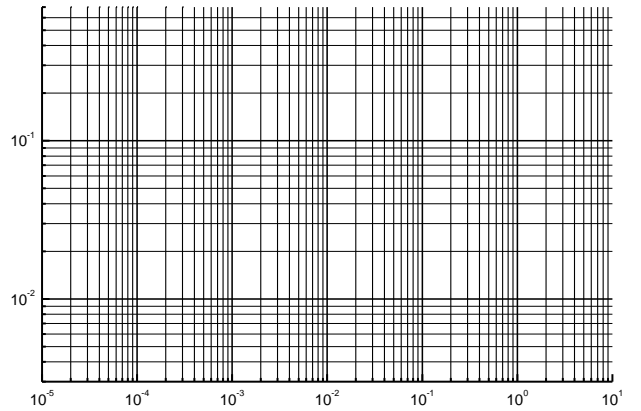
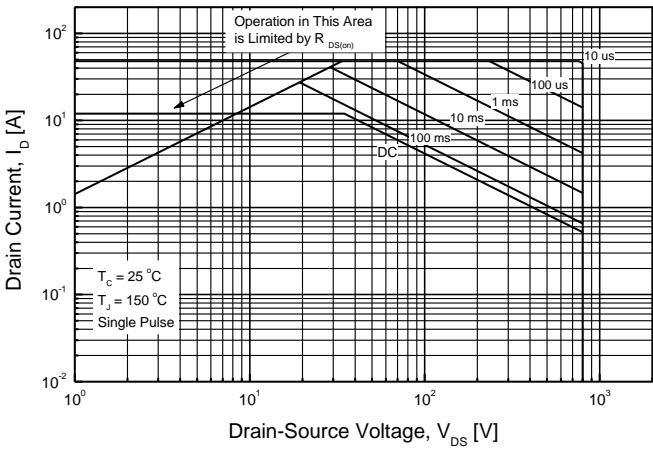
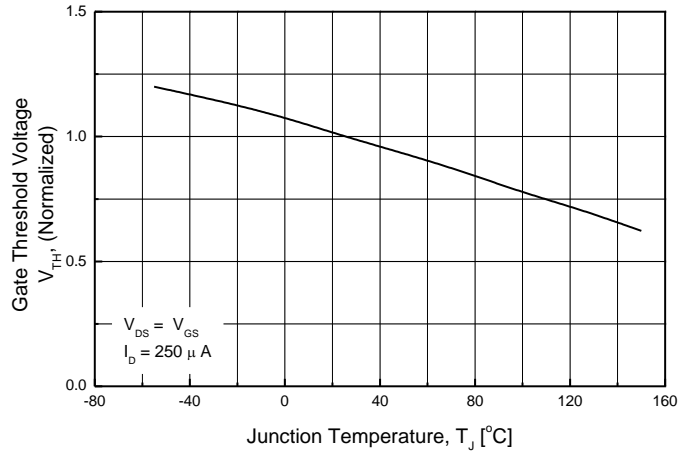
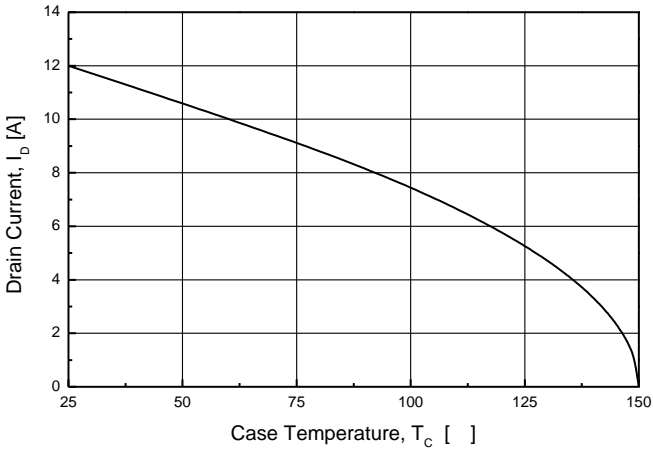
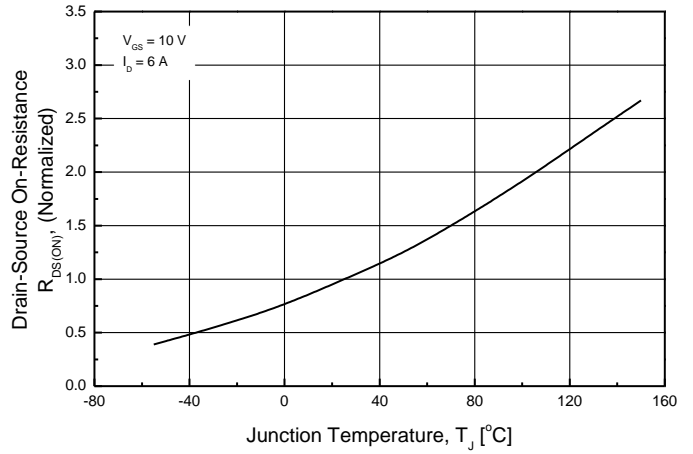
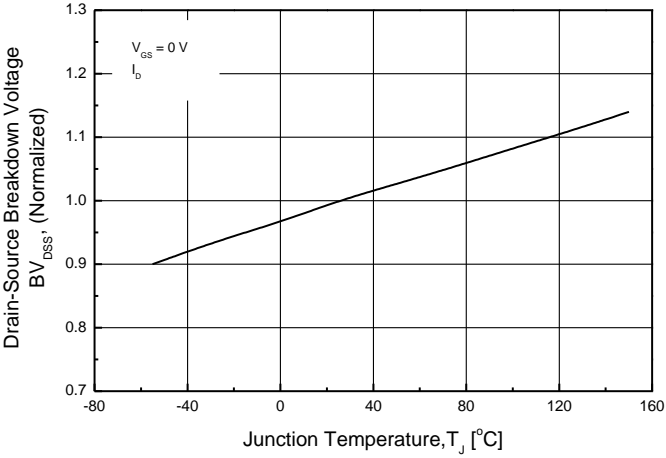
Note :

1. Repeated rating : Pulse width limited by safe operating area
2. $L=4.3\text{mH}, I_{AS} = 12\text{A}, V_{DD} = 50\text{V}, R_G = 25$, Starting $T_j = 25$
3. $I_{SD} = 12\text{A}, di/dt = \mu\text{s}, V_{DD} = 50\text{V}, V_{DS} = 640\text{V},$ Starting $T_j = 25$
5. Essentially Independent of Operating Temperature Typical Characteristics

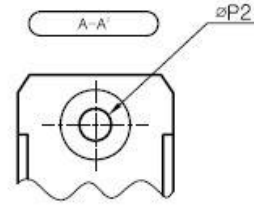
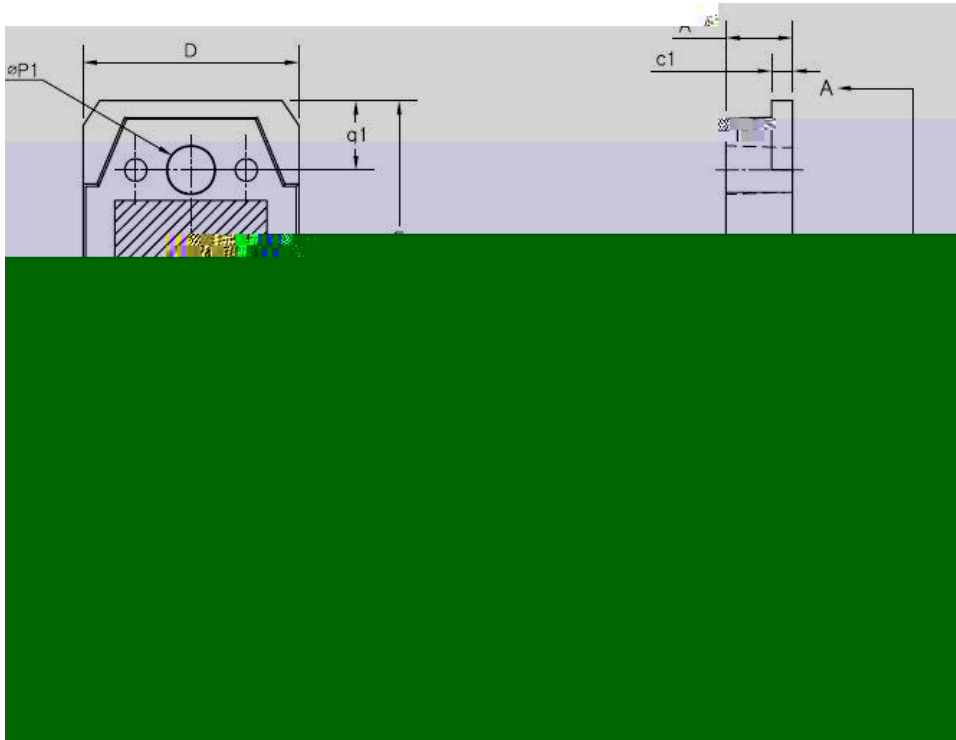


Drain Current, I_D





TO-3PN MECHANICAL DATA



SYMBOL	MIN	NOM	MAX
A	4.60	4.80	5.00
øP1	3.30	3.40	3.50
Q	2.20	2.40	2.60
q1	4.80	5.00	5.20