

## Features

- Low gate charge
- Improved dv/dt capability
- Improved ESD performance
- RoHS compliant
- JEDEC Qualification

## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	900	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Continuous Drain Current	$I_D$	$T_C = 25$	9*
		$T_C = 100$	5.9 *
Pulsed Drain Current <sup>(Note 1)</sup>	$I_{DM}$	36*	A
Single Pulse Avalanche Energy <sup>(Note 2)</sup>	$E_{AS}$	454	mJ
Repetitive Avalanche Current <sup>(Note 1)</sup>	$I_{AR}$	9	A
Repetitive Avalanche Energy <sup>(Note 1)</sup>	$E_{AR}$	29	mJ
Power Dissipation	$P_D$	$T_C = 25$	89
		Derate above 25	0.71
Peak Diode Recovery dv/dt <sup>(Note 3)</sup>	dv/dt	4.5	V/ns
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	
Maximum lead temperature for soldering purposes,	$T_L$	300	

## Thermal Characteristics

## Electrical Characteristics : $T_C=25$ , unless otherwise noted

Parameter	Symbol	Test condition	Min	Typ	Max	Unit
<b>OFF</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	900	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 900\text{ V}, V_{GS} = 0\text{ V}$	--	--	10	$\mu\text{A}$
		$V_{DS} = 720\text{ V}, T_C = 125\text{ }^\circ\text{C}$	--	--	100	$\mu\text{A}$
Forward Gate-Source Leakage Current	$I_{GSSF}$	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	$\mu\text{A}$
Reverse Gate-Source Leakage Current	$I_{GSSR}$	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	$\mu\text{A}$

### ON

Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	2.0	--	4.0	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 4.5\text{ A}$	--	1.12	1.4	$\Omega$
Forward Transconductance <sup>(Note 4)</sup>	$g_{FS}$	$V_{DS} = 30\text{ V}, I_D = 4.5\text{ A}$	--	17	--	S

### DYNAMIC

Input Capacitance	$C_{iss}$	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	2740	--	pF
Output Capacitance	$C_{oss}$		--	192	--	pF
Reverse Transfer Capacitance	$C_{rss}$		--	27	--	pF

### SWITCHING

Turn-On Delay Time <sup>(Note 4,5)</sup>	$t_{d(on)}$	$V_{DD} = 450\text{ V}, I_D = 9\text{ A},$ $R_G = 25$	--	52	--	ns
Turn-On Rise Time <sup>(Note 4,5)</sup>	$t_r$		--	97	--	ns
Turn-Off Delay Time <sup>(Note 4,5)</sup>	$t_{d(off)}$		--	212	--	ns
Turn-Off Fall Time <sup>(Note 4,5)</sup>	$t_f$		--	159	--	ns
Total Gate Charge <sup>(Note 4,5)</sup>	$Q_g$	$V_{DS} = 720\text{ V}, I_D = 9\text{ A},$ $V_{GS} = 10\text{ V}$	--	72	--	nC
Gate-Source Charge <sup>(Note 4,5)</sup>	$Q_{gs}$		--	11	--	nC
Gate-Drain Charge <sup>(Note 4,5)</sup>	$Q_{gd}$		--	31	--	nC

### SOURCE DRAIN DIODE

Maximum Continuous Drain-Source Diode Forward Current	$I_S$	----	--	--	9.0	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{SM}$	----	--	--	38	A
Drain-Source Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0\text{ V}, I_S = 9\text{ A}$	--	--	1.5	V
Reverse Recovery Time <sup>(Note 4)</sup>	$t_{rr}$	$V_{GS} = 0\text{ V}, I_S = 9\text{ A}$	--	570	--	ns
Reverse Recovery Charge <sup>(Note 4)</sup>	$Q_{rr}$	$di_F / dt = 100\text{ A}/\mu\text{s}$	--	6.6	--	$\mu\text{C}$

Note :

1. Repeated rating : Pulse width limited by safe operating area
2.  $L=10.6\text{mH}, I_{AS} = 9\text{A}, V_{DD} = 50\text{V}, R_G = 25$  , Starting  $T_J = 25$  , not subject to production test verified by design/characterization
3.  $I_{SD} = 9\text{A}, di/dt = 100\ \mu\text{s}, V_{DD} = 50\text{V}, V_{DS} = 720\text{V},$  Starting  $T_J = 25$   $^\circ\text{C}$
5. Essentially Independent of Operating Temperature Typical Characteristics

Fig. 1 Output Characteristics

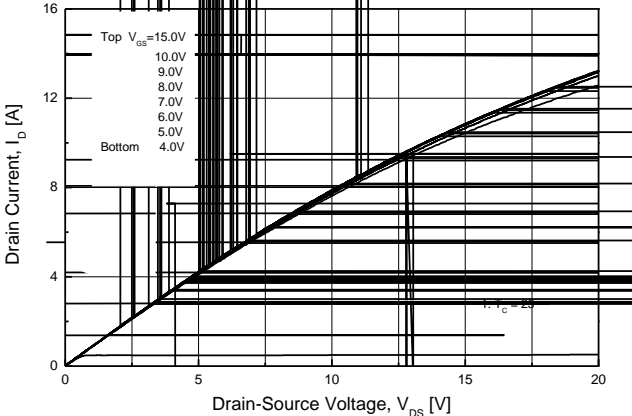


Fig. 2 Transfer Characteristics

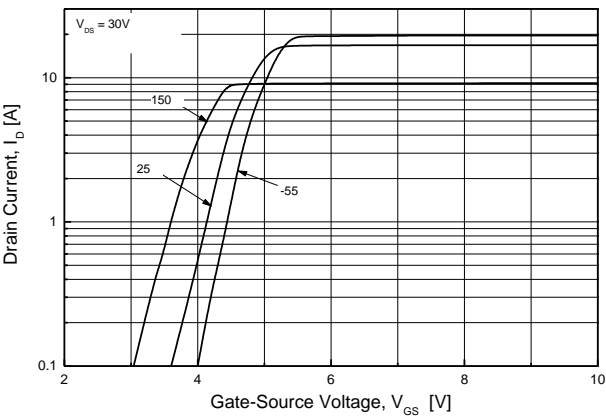


Fig. 3 On-Resistance vs. Drain Current and Gate voltage

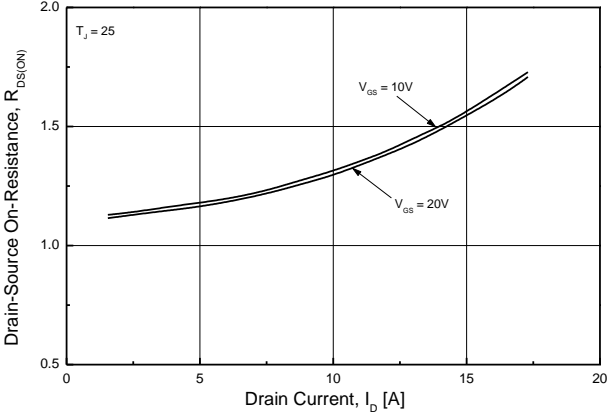


Fig. 4 Body Diode Forward Voltage vs. Source Current and Temperature

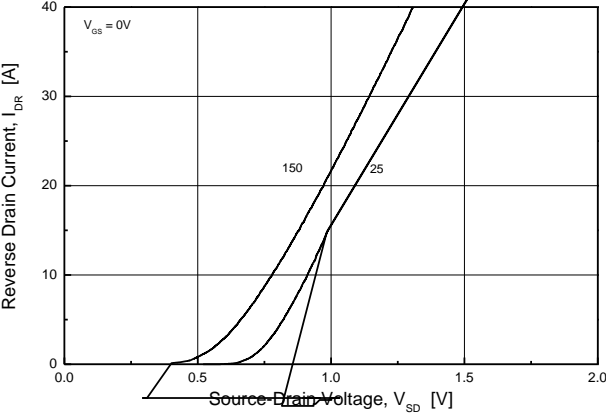


Fig. 5 Capacitance Characteristics

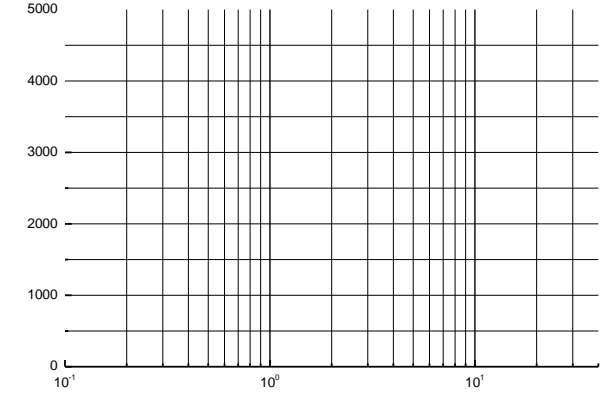


Fig. 6 Gate Charge Characteristics

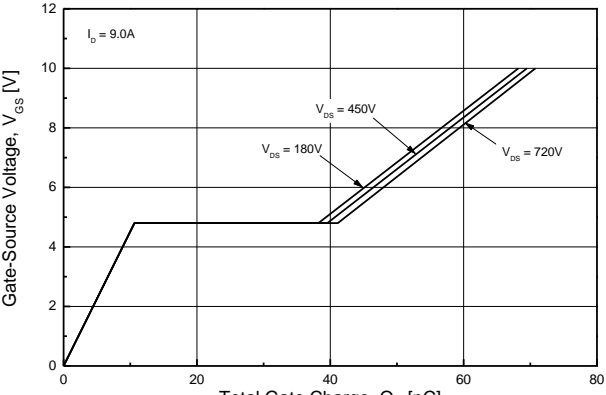


Fig. 7 Breakdown Voltage vs. Temperature

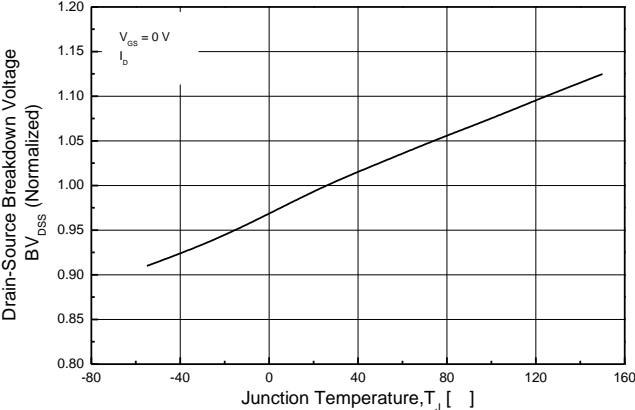


Fig. 8 On-Resistance vs. Temperature

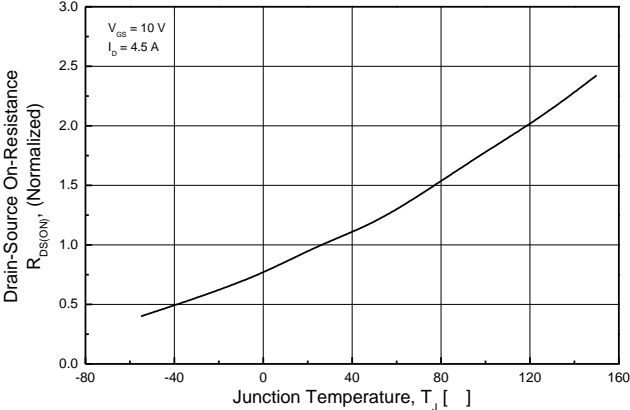


Fig. 9 Maximum Drain Current vs. Case Temperature

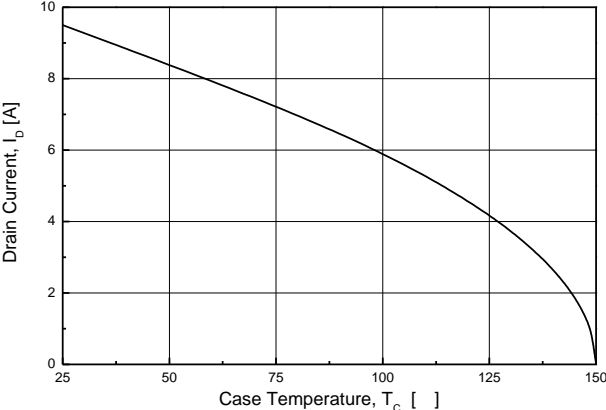


Fig. 10 Gate Threshold Voltage vs. Junction Temperature

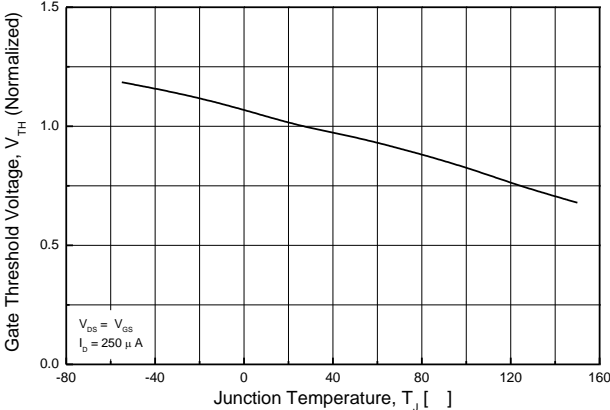


Fig. 11 Maximum Safe Operating Area

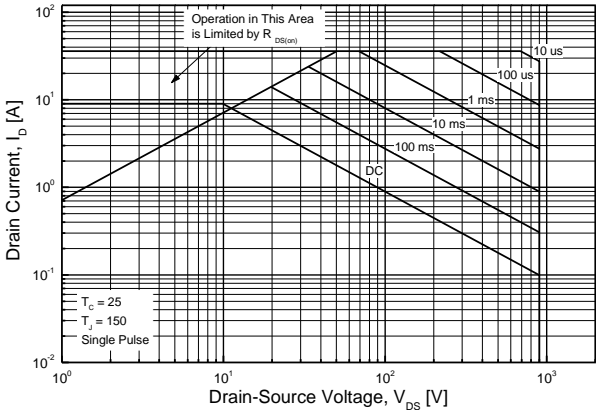
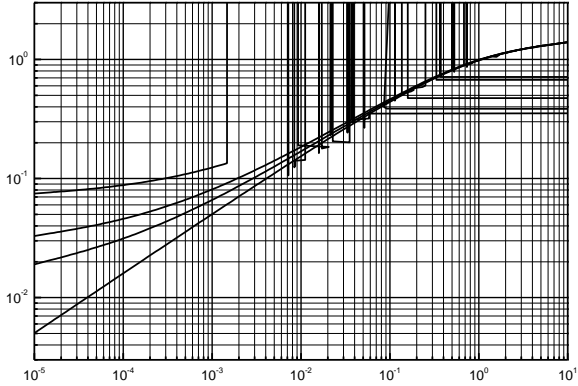
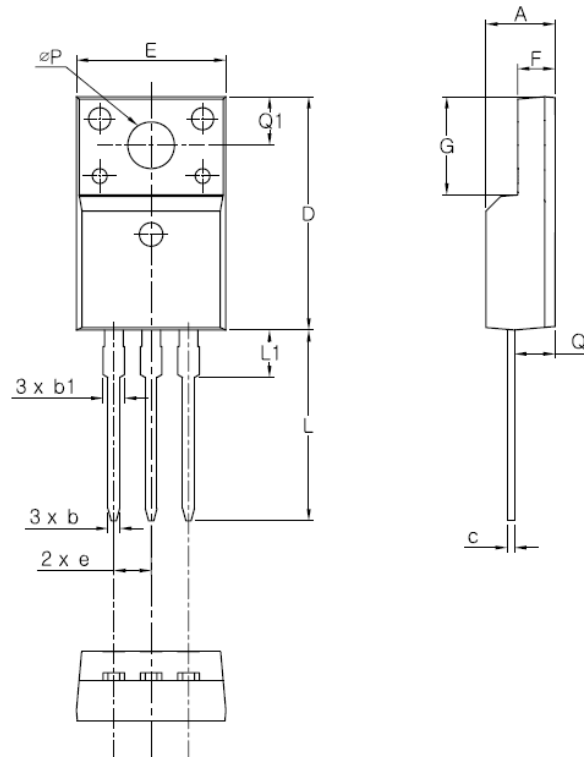


Fig. 12 Transient Thermal Response Curve



**TO-220F-3L MECHANICAL DATA****Disclaimer :**

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