

Features

- fLow gate charge
- f100% avalanche tested
- fImproved dv/dt capability
- fRoHS compliant
- fHalogen free package
- fJEDEC Qualification
- fImproved ESD performance

Absolute Maximum Ratings

Parameter	Symbol	TMP7N65AZ(G)	TMPF7N65AZ(G)	Unit
Drain-Source Voltage	$V_{DSS}$	650		V
Gate-Source Voltage	$V_{GS}$	-30		V
Continuous Drain Current	$T_C = 25$			

Electrical Characteristics :  $T_C=25$  , unless otherwise noted

Parameter	Symbol	Test condition	Min	Typ	Max	Units
<b>OFF</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	650	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	$\mu\text{A}$
		$V_{DS} = 520\text{ V}, T_C = 125^\circ\text{C}$	--	--	10	$\mu\text{A}$
Forward Gate-Source Leakage Current	$I_{GSSF}$	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	$\mu\text{A}$
Reverse Gate-Source Leakage Current	$I_{GSSR}$	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	$\mu\text{A}$

## ON

Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	3	--	5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 3.25\text{ A}$	--	1.2	1.4	$\Omega$
Forward Transconductance <sup>(Note 4)</sup>	$g_{FS}$	$V_{DS} = 30\text{ V}, I_D = 3.25\text{ A}$	--	10	--	S

## DYNAMIC

Input Capacitance	$C_{iss}$	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	1072	--	pF
Output Capacitance	$C_{oss}$		--	103	--	pF
Reverse Transfer Capacitance	$C_{rss}$		--	12	--	pF

## SWITCHING

Turn-On Delay Time <sup>(Note 4,5)</sup>	$t_{d(on)}$	$V_{DD} = 325\text{ V}, I_D = 6.5\text{ A},$ $R_G = 25$	--	35	--	ns
Turn-On Rise Time <sup>(Note 4,5)</sup>	$t_r$		--	46	--	ns
Turn-Off Delay Time <sup>(Note 4,5)</sup>	$t_{d(off)}$		--	82	--	ns
Turn-Off Fall Time <sup>(Note 4,5)</sup>	$t_f$		--	27	--	ns
Total Gate Charge <sup>(Note 4,5)</sup>	$Q_g$	$V_{DS} = 520\text{ V}, I_D = 6.5\text{ A},$ $V_{GS} = 10\text{ V}$	--	22	--	nC
Gate-Source Charge <sup>(Note 4,5)</sup>	$Q_{gs}$		--	5	--	nC
Gate-Drain Charge <sup>(Note 4,5)</sup>	$Q_{gd}$		--	10	--	nC

## SOURCE DRAIN DIODE

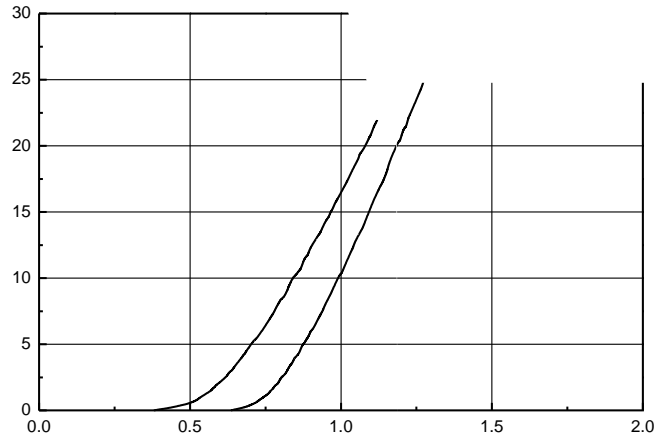
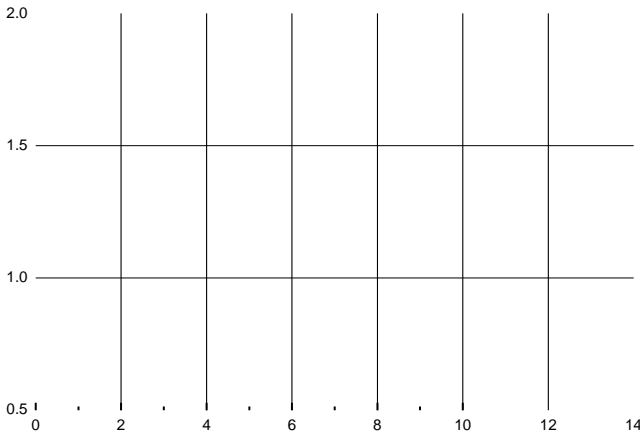
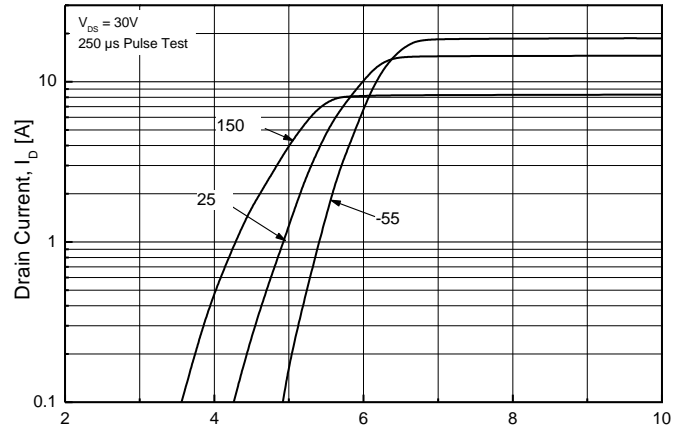
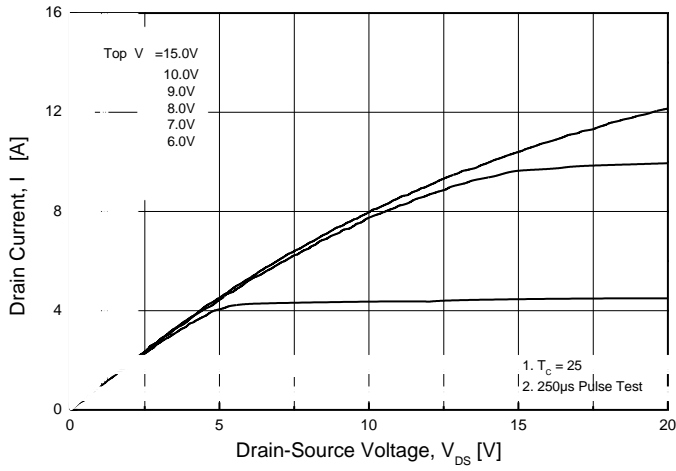
Maximum Continuous Drain-Source Diode Forward Current	$I_S$	----	--	--	6.5	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{SM}$	----	--	--	26	A
Drain-Source Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0\text{ V}, I_S = 6.5\text{ A}$	--	--	1.5	V
Reverse Recovery Time <sup>(Note 4)</sup>	$t_{rr}$	$V_{GS} = 0\text{ V}, I_S = 6.5\text{ A}$ $di_F / dt = 100\text{ A}/\mu\text{s}$	--	345	--	ns
Reverse Recovery Charge <sup>(Note 4)</sup>	$Q_{rr}$		--	2.6	--	$\mu\text{C}$

Note :

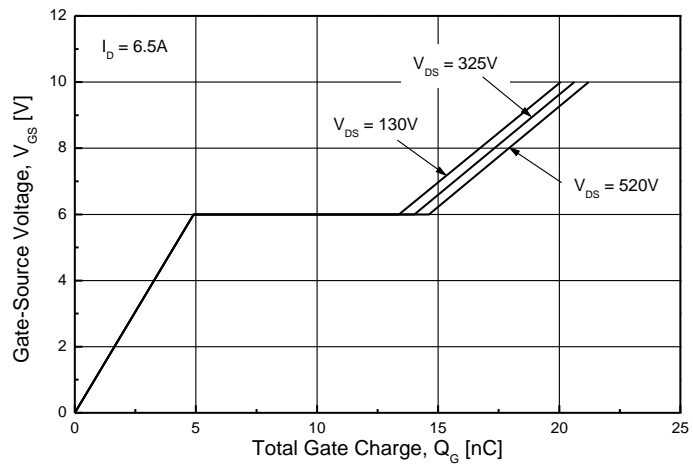
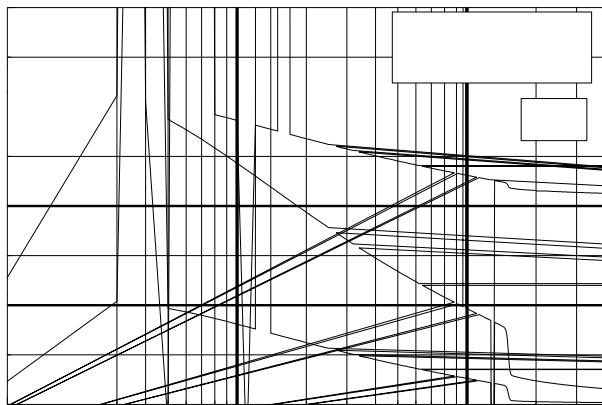
1. Repeated rating : Pulse width limited by safe operating area
2.  $L=17.1\text{ mH}, I_{AS} = 6.5\text{ A}, V_{DD} = 50\text{ V}, R_G = 25$  , Starting  $T_J = 25$
3.  $I_{SD} = 6.5\text{ A}, di/dt = 200\text{ A}/\mu\text{s}, V_{DD} = BV_{DS}$ , Starting  $T_J = 25$
4. Pulse Test : Pulse width  $300\ \mu\text{s}$ , Duty Cycle 2%
5. Essentially Independent of Operating Temperature Typical Characteristics



# TMP7N65AZ(G)/TMPF7N65AZ(G)

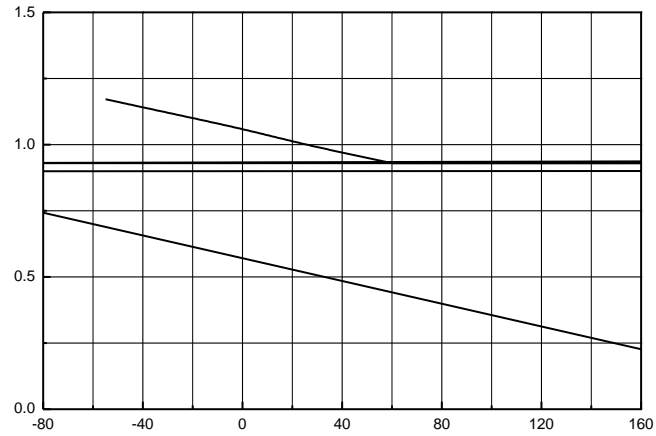
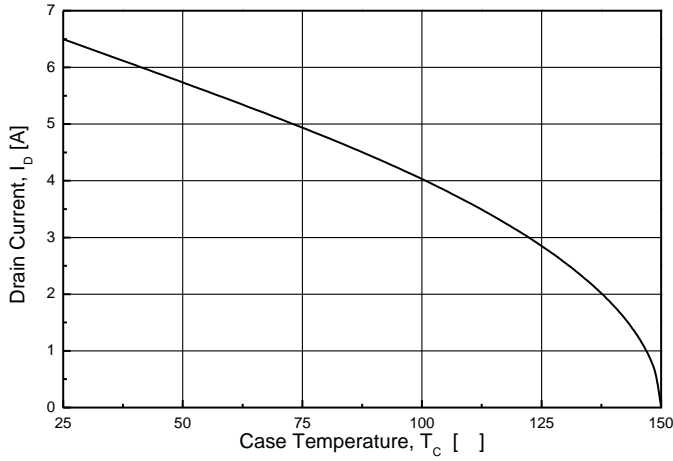
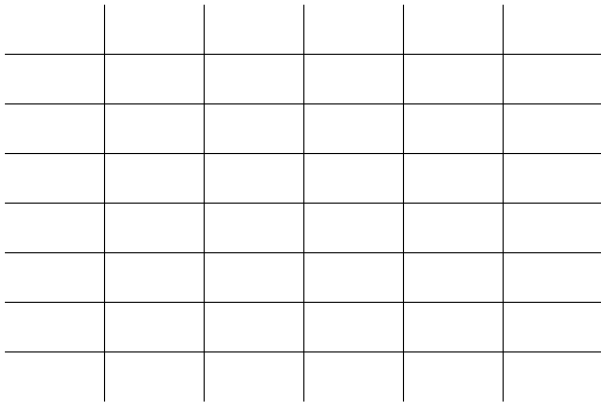


SD

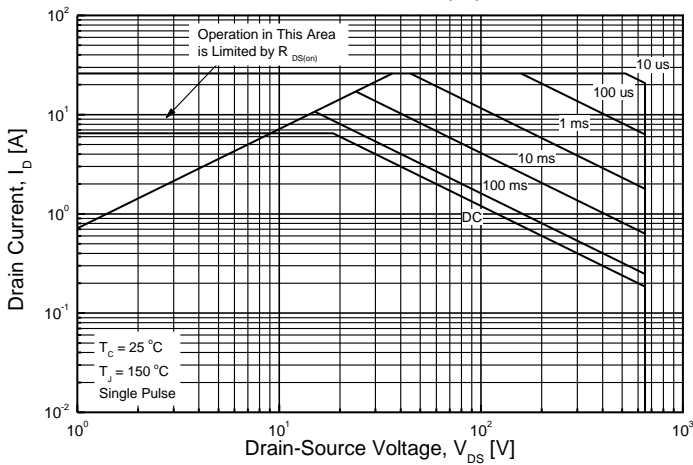




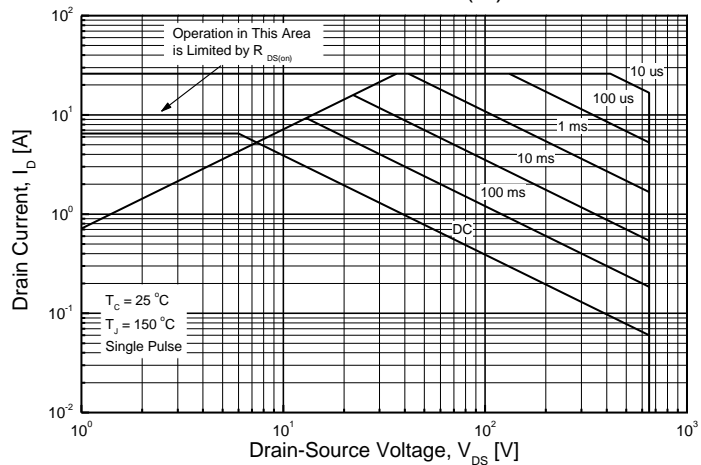
# TMP7N65AZ(G)/TMPF7N65AZ(G)



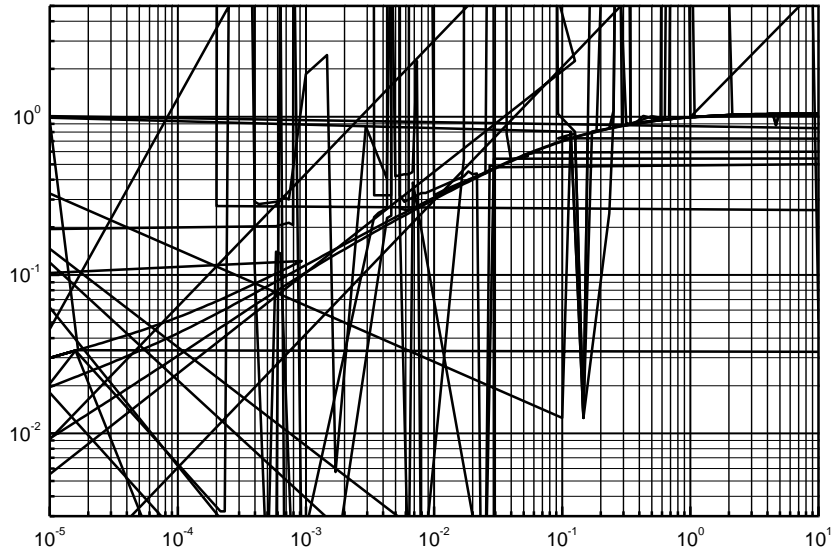
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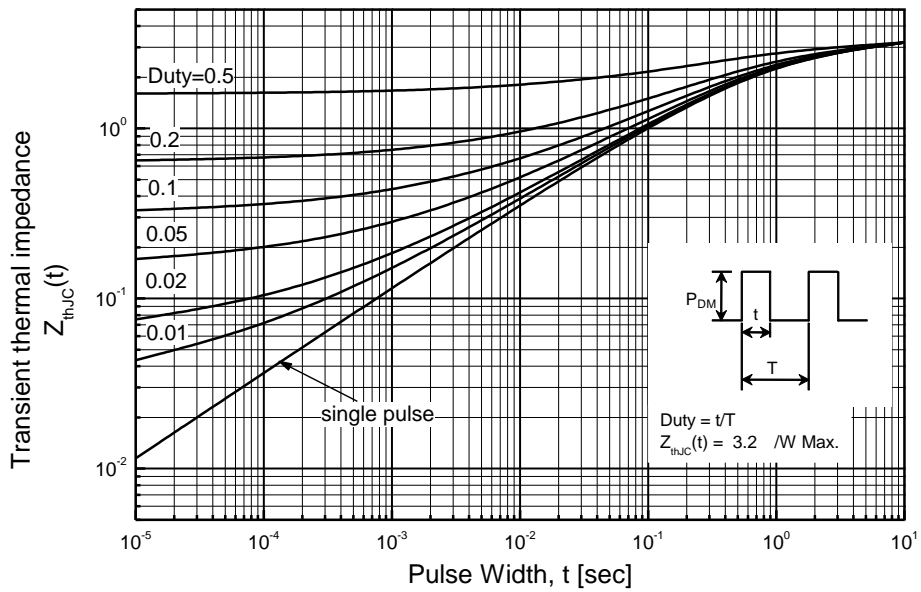
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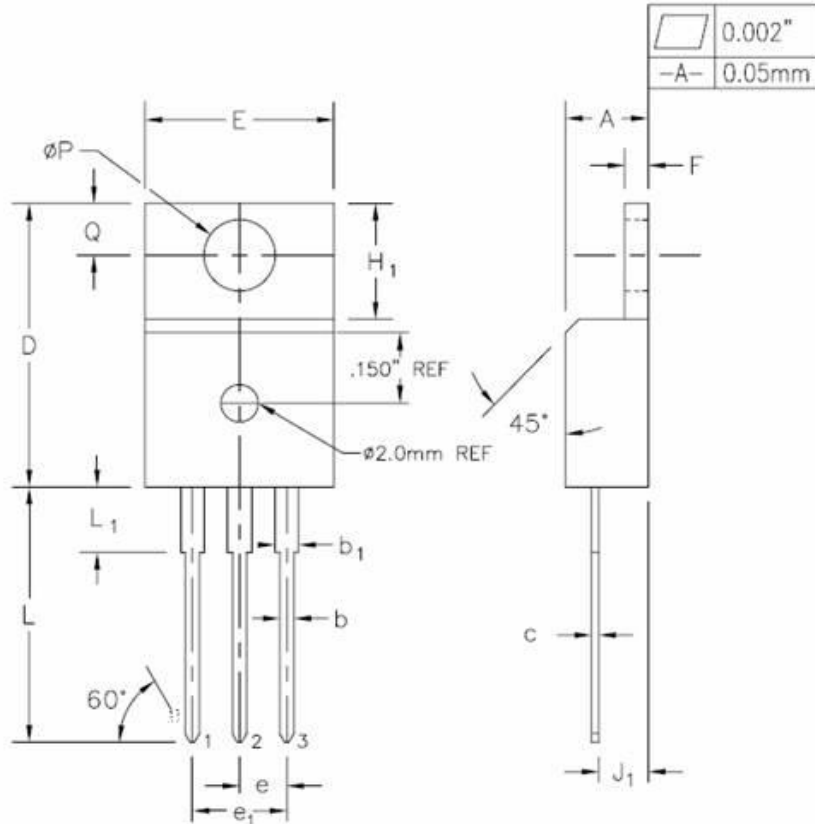
TMP7N65AZ(G)



TMPF7N65AZ(G)



TO-220AB-3L MECHANICAL DATA



SYMBOL	INCHES		MILLIMETERS		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	0.170	0.180	4.32	4.57	
b	0.028	0.036	0.71	0.91	
b <sub>1</sub>	0.045	0.055	1.15	1.39	
c	0.014	0.021	0.36	0.53	
D	0.590	0.610	14.99	15.49	
E	0.395	0.410	10.03	10.41	
e		0.100 TYP.		2.54 TYP.	
e <sub>1</sub>		0.200 BSC		5.08 BSC	
F <sub>1</sub>	0.048	0.054	1.22	1.37	
H <sub>1</sub>	0.235	0.255	5.97	6.47	
J <sub>1</sub>	0.100	0.110	2.54	2.79	
L	0.530	0.550	13.47	13.97	
L <sub>1</sub>	0.130	0.150	3.31	3.81	
2	∅P	0.140	3.55	3.81	
Q	0.102	0.112	2.60	2.84	



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