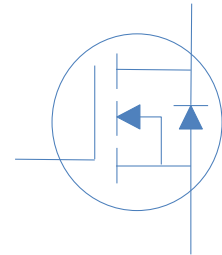


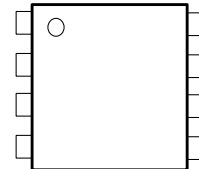


45V N-Ch Power MOSFET

V_{DS}		45	V
$R_{DS(on),typ}$	$V_{GS}=10V$	6.7	$m\Omega$
$R_{DS(on),typ}$	$V_{GS}=4.5V$	9.3	$m\Omega$
I_D (Silicon Limited)		52	A
I_D (Package Limited)		30	A



Part Number	Package	Marking
HGN095NE4SL	DFN5*6	GN095NE4SL

Absolute Maximum Ratings at $T_J=25$ (unless otherwise specified)

Parameter	Symbol	Conditions	Value	Unit
Continuous Drain Current (Silicon Limited)	I_D	$T_C=25$	52	A
		$T_C=100$	33	
		$T_C=25$	30	
Continuous Drain Current (Package Limited)		$T_C=25$	30	
Drain to Source Voltage	V_{DS}	-	45	V
Gate to Source Voltage	V_{GS}	-	± 20	V
Pulsed Drain Current	I_{DM}	-	120	A
Avalanche Energy, Single Pulse	E_{AS}	$L=0.4mH, T_C=25$	20	mJ
Power Dissipation	P_D	$T_C=25$	50	W
Operating and Storage Temperature	T_J, T_{stg}	-	-55 to 150	

Absolute Maximum Ratings

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-Ambient	$R_{\theta JA}$	55	W
Thermal Resistance Junction-Case	$R_{\theta JC}$	2.5	W

Electrical Characteristics at $T_J=25$ (unless otherwise specified)

Static Characteristics

Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	45	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.0	1.55	2.2	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS}=0V, V_{DS}=100V, T_J=25$	-	-	1	μA
		$V_{GS}=0V, V_{DS}=100V, T_J=100$	-	-	100	
Gate to Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Drain to Source on Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$	-	6.7	9.5	m Ω
		$V_{GS}=4.5V, I_D=10A$	-	9.3	14	
Transconductance	g_{fs}	$V_{DS}=5V, I_D=20A$	-	25	-	S
Gate Resistance	R_G	$V_{GS}=0V, V_{DS}$ Open, $f=1MHz$	-	1.5	-	Ω

Dynamic Characteristics

Input Capacitance	C_{iss}	$V_{GS}=0V, V_{DS}=20V, f=1MHz$	-	942	-	pF
Output Capacitance	C_{oss}		-	309	-	
Reverse Transfer Capacitance	C_{riss}		-	29	-	
Total Gate Charge	$Q_g(10V)$	$V_{DD}=20V, I_D=10A, V_{GS}=10V$	-	14.5	-	nC
Total Gate Charge	$Q_g(4.5V)$		-	7	-	
Gate to Source Charge	Q_{gs}		-	2	-	
Gate to Drain (Miller) Charge	Q_{gd}		-	2.5	-	
Turn on Delay Time	$t_{d(on)}$	$V_{DD}=50V, I_D=8A, V_{GS}=10V,$ $R_G=10\Omega,$	-	6	-	ns
Rise time	t_r		-	5	-	
Turn off Delay Time	$t_{d(off)}$		-	21	-	
Fall Time	t_f		-	5	-	

Reverse Diode Characteristics

Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_F=20A$	-	0.9	1.2	V
Reverse Recovery Time	t_{rr}	$V_R=20V, I_F=10A, di_F/dt=200A/\mu s$	-	24	-	ns
Reverse Recovery Charge	Q_{rr}		-	19	-	nC



Figure 3. On-Resistance vs. Drain Current and Gate Voltage

Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage

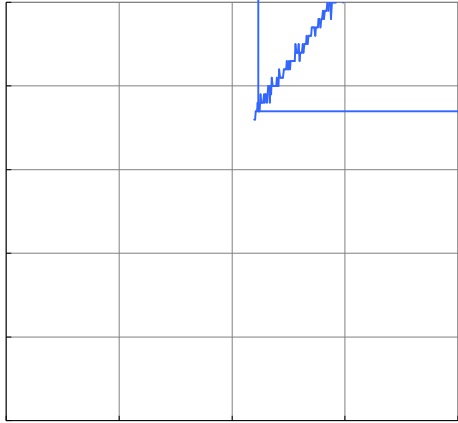


Figure 8. Typical Capacitance vs. Drain-to-Source Voltage

Figure 9. Maximum Safe Operating Area

Figure 10. Maximun Drain Current vs. Case Temperature

Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Ambient

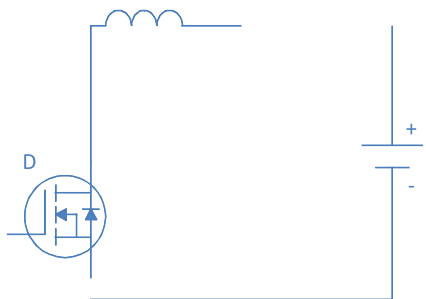
Inductive switching Test

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Gate Charge Test

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Uclamped Inductive Switching (UIS) Test



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Diode Recovery Test

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DFN5x6_P, 8 Leads